

Electro-optic modulation in integrated photonics

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Electro-optic modulators are an indispensable part of photonic communication systems, largely dictating the achievable transmission rate. Recent advances in materials and fabrication/processing techniques have brought new elements and a renewed dynamic to research on optical modulation. Motivated by the new opportunities, this Perspective reviews the state of the art in integrated electro-optic modulators, covering a broad range of contemporary materials and integrated platforms. To provide a better overview of the status of current modulators, an assessment of the different material platforms is conducted on the basis of common performance metrics: extinction ratio, insertion loss, electro-optic bandwidth, driving voltage, and footprint. The main physical phenomena exploited for electro-optic modulation are first introduced, aiming to provide a self-contained reference to researchers in physics and engineering. Additionally, we take care to highlight topics that can be overlooked and require attention, such as the accurate calculation of carrier density distribution and energy consumption, the correct modeling of thin and two-dimensional materials, and the nature of contact electrodes. Finally, a future outlook for the different electro-optic materials is provided, anticipating the research and performance trends in the years to come.

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I. INTRODUCTION

It has been over 50 years since the early research developments in lightwave transmission systems and almost 45 since the first commercial deployments. The last 20 years have undoubtedly witnessed an explosive development of the field,^{1,2} with the coming years anticipated equally bright. Light transmission systems penetrate the full spectrum of high-speed communications, from long-reach systems such as transoceanic links down to short-reach interconnects in datacenters^{3,4} and on-chip optical interconnects.⁵ The transmission capacity of such systems is mainly related to the optical modulation used, apart from various limiting factors imposed by the transmission channel and the associated impairments. In this respect, the optical modulator is one of the key elements and research in this field has been very active ever since the emergence of optical communications. Publications on optical modulation exceeded 1000 per year since the early 90's and are constantly above 5000 per year since 2010 (Scopus). Optical modulators constitute a multi-disciplinary field in which physics, materials science, engineering design, fabrication processes, and packaging are all of critical importance.

Optical modulators imprint an electrical signal on an optical wave, termed the optical carrier. Amplitude, phase, frequency, and polarization of the optical carrier and any combination of the above can be exploited for encoding the information. Modulators can be of integrated or free-space type: in the former category the optical waves are guided modes in a photonic integrated circuit (PIC), whereas in the latter they are free-space propagating beams. This review will almost exclusively deal with modulation in integrated circuits, with the

elementary structure being a waveguide. Free-space optical modulators are also of high importance to a broad range of applications and can be based on multi-layers, metasurfaces, diffraction gratings or other free-space analogues.⁶⁻¹¹

Optical modulation requires an electro-optic (EO) effect, which provides the necessary change in the optical properties of certain materials when subjected to electric fields of frequency substantially below that of the light wave. When the EO effect modifies the refractive index (equivalently, the real part of optical permittivity), the modulator is of electro-refraction type and this is the case with the Pockels effect, the Kerr electro-optic (or DC Kerr) effect, and free-carrier dispersion (plasma effect).¹² On the other hand, when material losses are modified, the modulator is of electro-absorption type and this is commonly provided by the Franz-Keldysh effect in bulk semiconductors, the quantum-confined Stark effect in quantum-well structures, and free-carrier absorption.¹³⁻¹⁵ Electro-refraction modulators employ an interferometer (most commonly a Mach-Zehnder structure) or a resonator (such as a microring). Electro-absorption modulators are straight waveguide sections. In this Perspective, we will mainly cover external modulators and only shortly discuss direct modulation based on the III-V material system in Section V.

Modulator performance is typically quantified in terms of a number of metrics, which include the extinction ratio (ER), insertion losses (IL), electro-optic bandwidth (EO BW), optical bandwidth, linearity, driving voltage, energy consumption, chirp, and footprint.^{12,15,16} ER quantifies the contrast between the high-transmission and low-transmission levels; it is a measure of the suppression (extinction) of the light wave when the modulator is in the non-transmissive state and should acquire values as high as possible. IL measures the losses suffered by the light wave when the modulator is in the high-transmission state and should acquire values as low as possible. EO BW is

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of utmost importance as it quantifies if the modulator can cope with rapid variations of the modulating electric signal; this directly relates to the highest bit rate that can be supported. The optical bandwidth corresponds to the wavelength range that allows for satisfactory modulation performance. Linearity is commonly quantified in terms of the spurious free dynamic range (SFDR), which is measured in a two-tone excitation. The driving voltage corresponds to the amplitude of the modulating signal and for phase modulators it is quantified with reference to the half-wave voltage, i.e., the necessary voltage to induce a π phase shift (V_π). Energy consumption is also an important performance indicator and reports the mean energy that is lost per bit; it is theoretically estimated rather than measured, and in many cases the predictions can be inaccurate when over-simplified models are employed, as it will be discussed. Energy consumption relates to the voltage of the modulating signal, amongst other parameters, and both consumption and the necessary voltage level should be kept as low as possible. Chirp relates to instantaneous frequency changes that are additionally imprinted on the optical carrier; along with the dispersive effects taking place in fiber transmission, they dictate pulse broadening. Ideally, a modulator should introduce zero chirp, though in specific cases predefined chirp levels can be also available. Finally, the footprint is the physical area occupied by the modulator and should be kept minimal to align with the constantly increasing demands of size shrinkage and higher integration densities in PICs.

In the literature, authors alternatively choose to compare modulators on the basis of various figures of merit that combine more than one of the metrics introduced above and thus consider inherent trade-offs in performance. Very popular are the figures of merit combining modulation efficiency together with losses ($V_\pi L a_{\text{dB}}$) as well as bandwidth together with energy consumption or voltage. Bandwidth itself is very often reported in terms of the maximum achievable line rate (bit rate), especially in the case of experimentally evaluated modulators. However, it should be noted that it is the EO bandwidth that is the inherent modulator characteristic, unambiguously determining its frequency response. The achievable line rate is additionally determined by the modulation format and coding employed, the digital signal processing (DSP) techniques used on the transmitter and receiver side, and error correction algorithms. All of the above enable an increase in line rate, especially when the available bandwidth is limited. The mainstream choice from the earliest generations of lightwave systems has been intensity modulation (on-off keying) with direct detection (IM/DD) and it is still dominating access networks. On the other hand, advanced modulation formats with improved spectral efficiency and transmission capacity are used in both IM/DD systems (PAM/ASK modulation) and optical digital coherent systems^{17–19} that dominate core/metro networks (long-haul), with QPSK and QAM modulations being very popular and extensively deployed in the latter case. The coherent and conventional IM/DD systems can be identified as the two main system types but also other alternatives have been proposed especially for short-reach links; they utilize different modulation formats such as the discrete multi-tone (DMT), carrier-less

amplitude and phase (CAP) and half-cycle subcarrier modulation (SCM).^{20–24} The above formats are often employed for directly modulated lasers of limited bandwidth and are important to the family of advanced direct detection schemes. Advanced DD schemes offer a compromise between coherent and IM/DD systems, constituting a trade-off between system performance and complexity. The end performance depends on the modulation format employed, as highlighted above. However, a detailed discussion of modulation formats and their impact on the achievable bit rate is well beyond the content of this Perspective, which focuses on the physics, materials, and components for EO modulation. For this reason, special effort is made to review the different modulator types based on their inherent EO bandwidth instead of solely relying on the achieved line-rate values.

The paper is organized as follows: Section II briefly reviews the most important physical phenomena leading to amplitude and phase EO modulation, including the Pockels effect, the Franz-Keldysh and quantum-confined Stark effects, free-carrier effects (plasma dispersion) and phase-change effects. Section III provides a comprehensive review of recent advances in EO modulation with reference to the material system exploited, covering lithium niobate, III-V semiconductors, silicon, EO polymers, transparent conducting oxides, two-dimensional materials, and phase-change materials. This way of presentation highlights the important traits of different EO materials, that can be accommodated in diverse underlying platforms. Section IV comparatively assesses the various modulator types reviewed and discusses overlooked or misinterpreted topics important to the design and analysis of EO modulators. In Section V, direct modulation systems are briefly discussed, as an alternative to external modulation for short-range applications. Finally, Section VI provides a future outlook for all the considered modulator types and Section VII closes the Perspective with concluding remarks.

II. PHYSICAL PHENOMENA FOR AMPLITUDE AND PHASE ELECTRO-OPTIC MODULATION

Figure 1 summarizes the different instances of contemporary photonic modulators examined in this work. A broad range of physical phenomena has been exploited, suited to either amplitude or phase modulation. The basic configurations can be classified in three main categories: in-line modulators, resonant structures, and devices based on waveguide mode interference. In this section, the most prominent physical phenomena are briefly described.

A. Pockels and Kerr Electro-optic Effects

The term EO effect broadly describes refractive-index changes that are induced by the application of an external electric field that can be static or of low frequency compared to the optical frequency (quasi-static). In the linear EO effect or Pockels effect, the change in the elements of the impermeability tensor $\hat{\eta} = \hat{\epsilon}_r^{-1}$ (equivalently the difference between

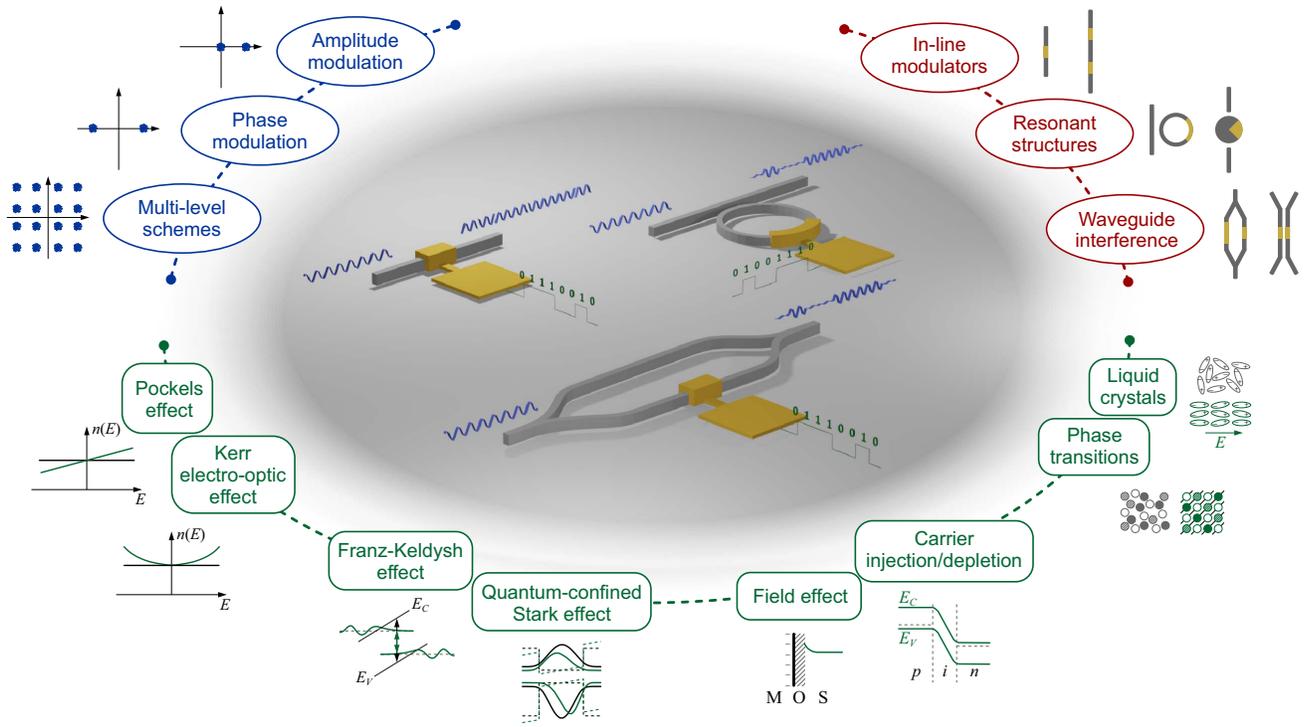


FIG. 1. Components and physical phenomena for amplitude and phase electro-optic modulation. Schematic illustration of in-line modulator performing phase modulation, ring resonator and Mach-Zehnder interferometer performing amplitude modulation.

the coefficients of the distorted index ellipsoid and the undistorted one) is a linear function of the externally applied electric field.^{25,26} This translates to index changes that are linearly proportional to the applied field. The Pockels effect is nonzero only in non-centrosymmetric crystalline materials and vanishes in amorphous ones. Well-studied EO materials include the crystals potassium dihydrogen phosphate (KH_2PO_4 or KDP), ammonium dihydrogen phosphate ($\text{NH}_4\text{H}_2\text{PO}_4$ or ADP), lithium niobate (LiNbO_3), lithium tantalate (LiTaO_3), barium titanate (BaTiO_3) as well as semiconductors such as gallium arsenide (GaAs), cadmium tellurite (CdTe), and zinc tellurite (ZnTe). Lithium niobate is a uniaxial crystal of particular importance to integrated photonics due to its very wide use and will be reviewed in Section III A.

The EO response in the Pockels effect is described by a set of coefficients that define the third-rank linear EO tensor r_{ijk} (27 elements); given the symmetry properties of the second-rank optical permittivity and impermeability tensors, the maximum number of independent elements in r_{ijk} is reduced to 18. These elements are arranged in a 6×3 matrix using a standard index reduction; some will further end up being zero considering the existing crystal symmetry properties. The independent EO coefficients in LiNbO_3 are 4: r_{33} , r_{13} , r_{22} , r_{51} .²⁷ When the external electric field is aligned along the optical (crystal) axis of a C_{3v} crystal such as LiNbO_3 or LiTaO_3 , the crystal remains uniaxial and the principal axes are preserved. The most efficient configuration for phase modulation is that of a transverse modulator, with the light wave (optical mode) propagating along the x axis, dominantly polarized along the optical axis z (extraordinary wave), and the external electric field that

induces the index changes through r_{33} being also parallel to the z axis.²⁶

The Kerr EO effect shares similarities with the Pockels effect but the changes in the impermeability tensor are now proportional to the second order of the applied electric field. This translates to index changes that are proportional to the square of the applied field.²⁵ The Kerr EO effect is found in crystalline as well as amorphous materials and typical examples include the KDP and ADP crystals as well as certain liquids.

B. Franz-Keldysh and Quantum-Confined Stark Effect

The application of an electric field to atoms and molecules shifts their energy states by changing the energy of their electrons (Stark effect). In bulk semiconductors or insulators, the field effect results in a tilt of their energy band edges, which is referred to as the Franz-Keldysh effect.²⁸ The electron (hole) wavefunctions near the conduction (valence) band edge do not correspond to plane waves anymore, but are described by Airy functions. As a result, they expand into the bandgap in an exponentially decreasing manner, occupying previously forbidden states. The latter is translated into an effective decrease of the bandgap value, rendering the material absorptive to previously transparent wavelengths (see inset of Fig. 1). The effect can be equivalently described as a photon-assisted tunneling process.

In contrast to the Franz-Keldysh effect, which is independent of the crystal size, the quantum-confined Stark ef-

fect (QCSE) manifests in quantum-confined carriers.²⁹ Electrons and holes can be confined in three-, two-, and one-dimensional potential wells in nanoscale structures such as quantum dots, wires, and wells, respectively. Such structures impede electrons and holes from tunneling out and ensure a strong electron-hole interaction. The latter ensures the formation of stable excitonic states at room temperature. Shifting these quantum-confined states through the application of an electric field (Stark effect) results in the QCSE (Fig. 1, inset). Multiple quantum well (MQW) structures are the most popular, realized by thin semiconductor layers sandwiched between sufficiently thick barrier layers of wider-bandgap semiconductor materials. The thicknesses of the wells and barriers are in the order of a few nanometers. The applied field should be perpendicularly polarized to the quantum wells, pulling electrons and holes towards opposite sides of the layer, thus reducing the energy of the electron-hole pair and red-shifting the excitonic resonance. The applied field can well exceed the value of the ionization field, resulting in large shifts, without significant broadening of the exciton peaks. In bulk crystals, the lack of quantum confinement results in a poor shift and severe broadening of the excitonic resonances for applied fields that exceed only a few times the ionization field.

Both effects are more pronounced in direct bandgap semiconductors due to the steep change in absorption. Their manifestation is stronger for photon energies near the bandgap value. For telecom applications, III-V semiconductors have been extensively employed due to their direct and suitably wide bandgap. The most studied representatives include the InP and GaAs systems as well as MQW structures utilizing for example the InGaAsP–InP platform. Germanium (Ge), a group IV semiconductor, has also attracted attention for NIR modulators due to its 0.8-eV direct bandgap as well as its compatibility with silicon. Despite being an indirect semiconductor (~ 0.66 eV), a strong electro-absorption effect has been demonstrated either through the Franz-Keldysh effect in Ge and $\text{Si}_{1-x}\text{Ge}_x$ alloys or the QCSE in Ge/SiGe quantum-well structures. Together with changes in absorption, both effects also give rise to changes in the refractive index (electro-refraction effect), as dictated by the Kramers-Kronig relations,³⁰ which can be exploited for realizing phase-shifting elements. The required electric field is usually applied through reverse-biased p-i-n junctions, allowing for high bandwidth and low energy consumption.

C. Free-Carrier Effect (Plasma Dispersion Effect)

The free-carrier effect, also widely referred to as the plasma dispersion effect, refers to the dependence of the optical properties (both optical refraction and absorption) on the concentration of free carriers;³¹ this effect nominally appears in semiconductors^{32–34} and semimetals.³⁵ Changes in the concentration of free carriers affects the optical absorption of the material but it inevitably alters its dielectric properties as well, as dictated by the fundamental Kramers-Kronig relations. Hence, both amplitude and phase control of the guided wave is possible, offering opportunities for different modulation re-

alizations. Initially, the practical development of functional integrated photonic devices working with the free-carrier effect was conceptually straightforward due to the broad use of silicon and III-V semiconductors in electronics. Schemes like p-n and p-i-n junctions or metal-oxide-semiconductor (MOS) capacitors, already studied and well-understood, were widely used to introduce carrier depletion or accumulation regions. In recent years, alternative semiconductors and semimetals have also appeared (transparent conducting oxides and two-dimensional materials), nominally governed by the same operation principle but exhibiting enhanced optical properties.

The simplest configuration to achieve carrier depletion is a reverse-biased p-n junction, where a space-charge region is formed due to a free-carrier (electrons and holes) sweep-out, reducing the optical absorption and modifying the refractive index. On the contrary, in a forward-biased p-n junction, carriers are injected via the induced current flow. Low doping is preferred in carrier-depletion configurations to reduce optical losses, while for carrier injection schemes p-i-n junctions are favored for the same reason (lower optical losses in the intrinsic semiconductor region).³¹ Carrier-accumulation configurations employ a MOS-like structure, where a thin dielectric layer is sandwiched between semiconductors and/or (semi)metals. By applying an external voltage, opposite-charge carriers accumulate on each side of the capacitor. Typical dielectrics such as silicon dioxide are commonly used, while high- κ dielectrics can further intensify the effect. Unlike p-n/p-i-n junctions, which are typical for conventional semiconductor configurations, MOS-like capacitors are preferred in contemporary configurations, involving transparent conducting oxides or low-dimensional materials.^{36,37}

D. Phase-Change Effect

Phase-change materials (PCMs) can experience a phase transition in response to external stimuli (temperature, electric field, optical illumination). They are broadly classified in two main categories: (i) transition metal oxides that undergo a reversible crystalline-to-crystalline (electronic) transition and are suited to volatile applications such as amplitude/phase modulation and routing, and (ii) chalcogen-based alloys which undergo an amorphous-to-crystalline (structural) transition and are primarily exploited for non-volatile applications such as memory operation.³⁸ Prominent materials for the two categories, respectively, are vanadium dioxide (VO_2), which exhibits the phase change close to room temperature, and the ternary germanium-antimony-tellurium (GST) compound $\text{Ge}_2\text{Sb}_2\text{Te}_5$. In both cases, the phase transition can be driven electrically and is associated with a metal to insulator transition leading to a pronounced change in the infrared optical properties (refractive index, n , and extinction coefficient, κ).

III. CONTEMPORARY PHOTONIC ELECTRO-OPTIC MODULATORS

In this Section, we review the advances in contemporary photonic electro-optic modulators, categorized based on the EO material they exploit.

A. Lithium Niobate

The lithium-niobate (LN) modulator is one of the most successful and indispensable components in optoelectronics. For decades, lithium niobate has been the material of choice, featuring a very strong Pockels effect (the strongest element in the EO coefficient tensor of bulk LiNbO_3 being $r_{33} \sim 31$ pm/V),²⁷ broadband spectral response with refractive-index change at the femtosecond timescale, wide transparency window (0.4-5.5 μm), linearity, good temperature stability with a low thermo-optic coefficient (TOC) of 10^{-5} K⁻¹, excellent chemical and mechanical stability as well as long-term reliability.

In-diffusion of titanium into bulk LN wafers and annealed proton exchange are the commonly employed methods for defining conventional LN stripe waveguides,³⁹ mostly in Mach-Zehnder modulator (MZM) configurations. Such waveguides are characterized by low index contrast (~ 0.02 or less), translating into weak optical confinement in both horizontal and vertical directions and, thus, limited possibility for high density integration. Typical phase shifter lengths are in the cm range (> 5 cm) and V_π is 3 to 6 V, thus $V_\pi L$ normally exceeds 10 Vcm. Due to the poor mode confinement, metal electrodes should be spaced far away from the optical waveguide (approximately 10 μm) to keep losses at a reasonable level. This means that it is typically necessary to use an electrical amplifier to drive a conventional LN-MZM, which is itself energy consuming. The high driving voltages translate into high energy consumption.

Conventional LN modulators have been for years the standard choice for long-haul telecom systems, but they cannot meet the growing demands of short-range datacom systems, being too bulky, costly, and inefficient. Therefore, there is a steadily growing interest for high-performance, miniaturized LN modulators for datacom and data center applications, wireless communications, and quantum applications. Most commercial LN products nowadays still outperform Si-based modulators and offer a 3-dB BW up to 35 GHz, half-wave voltage V_π down to 3.5 V, and possibility for zero to negative chirp.

Evolution of the LN modulator was made possible with the fabrication of single-crystal LN thin films and LN wafers. The crystal ion slice (CIS) method was introduced in Ref. 40, where an LN substrate is ion-implanted and subsequently etched in hydrofluoric acid; initially separated slices were around 9 μm thick. Rapid progress in CIS and wafer bonding led to sub-micron LN films,⁴¹ where an ion-implanted substrate is thermally treated leading to a thin layer of lithium niobate being split off and subsequently transferred to another substrate. Transferred layers and bulk lithium-niobate

are of comparable quality and properties. Lithium-niobate-on-insulator (LNOI) platforms offer high index contrast in the range of ~ 0.7 and bending radii below 20 μm are now possible. LN thin films can be processed in 3-6" wafers, which is satisfactory though still falling behind the 8-12" wafers in silicon photonics, with all three crystal cuts (x -, y -, z -cut) possible and commercially available. In x -cut LN modulators operation is on the quasi-TE mode with the horizontal electric field sensing the large EO coefficient r_{33} , whereas z -cut modulators are intended for quasi-TM mode operation. LN thin-film devices are in the millimeter length scale (< 20 mm) and thus can fit in common transceiver packages such as the Quad Small-Factor Pluggable (QSFP). Even more intriguing is the possibility to operate with voltages ~ 1 V that allow driverless modulation from direct CMOS output.

Contemporary LN devices are broadly classified in two categories: those exploiting a uniform (un-etched) LN thin film and those with patterned (etched) LN films. The first category comprises a broad range of heterogeneously integrated devices or hybrid platforms.^{42,43} Uniform LN thin films can be oxide-bonded on prefabricated silicon waveguides at room temperatures and importantly the fabrication process is CMOS compatible,⁴⁴⁻⁴⁶ with LN integration performed at the back-end respecting the structure already formed. The underlying silicon-photonic circuitry provides the required passive functionality and ensures efficient standardized in/out chip coupling. In Ref. 44, an un-etched LN film having a thickness of 600 nm is bonded on top of a planarized silicon-on-insulator (SOI) photonic circuit accommodating a Mach-Zehnder interferometer (MZI), with aluminum electrodes defined over the LN film (Fig 2); a device 5 mm long demonstrated a $V_\pi L$ equal to 6.7 Vcm and an ER above 20 dB. More importantly, the measured EO response confirmed a bandwidth exceeding 106 GHz. Devices with two layers of silicon waveguides that are vertically coupled using adiabatic tapers result in lower losses and higher optical overlap with the LN thin film.⁴⁶ Other hybrid platforms choose an easy-to-etch material as the device layer (loading), which is bonded on top of an un-etched LN thin film. Rib waveguides are defined and loading materials include tantalum pentoxide,⁴⁷ chalcogenide glass,⁴⁸ and silicon nitride.⁴⁹⁻⁵¹ This approach appears promising, though various limitations exist including the incompatibility of some of the materials used (tantalum pentoxide, chalcogenides) with CMOS processing. Hybrid Si-LN waveguides may suffer from reduced light confinement in the LN core and detrimental nonlinearities originating from Si. Silicon nitride (SiN) is deemed a more appealing alternative to Si due to the absence of two-photon absorption (TPA), lower material loss, and broader transparency window; in addition SiN has a lower refractive index compared to Si, thus allowing for more light to be confined in the LN film.⁵⁰

The second category of devices includes those with etched single-crystal sub-micron lithium-niobate films.⁵²⁻⁵⁸ Lithium-niobate is considered a material hard to etch and in principle produces rough surfaces that suffer from high scattering losses, in the order of 3 dB/cm or higher.^{55,56} The possibility of ultra-low losses in dry-etched subwavelength LN waveguides was demonstrated in Ref. 52 with a value as low as

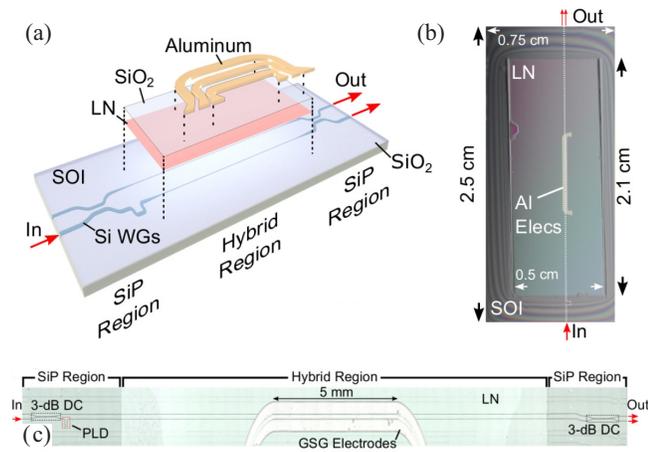


FIG. 2. Hybrid Si-LN EO Mach-Zehnder modulator.⁴⁴ (a) General view showing the un-etched (uniform) LN thin film bonded on top of an MZI written in Si and the aluminum electrodes. The “SiP Regions” accommodate the Si passive circuitry outside the bonded LN film area. (b) SEM image of the hybrid Si-LN modulator. (c) Composite microscope image (not to scale). DC: directional coupler, PLD: path-length difference, GSG: ground-signal-ground, SiP: Si photonics. Adapted with permission from Ref. 44. Copyright 2018 The Optical Society.

~ 0.027 dB/cm. In Ref. 54, an LN thin film of 600 nm thickness has been etched to define an MZI [Fig. 3(a)] and for a 20-mm-long device the half-wave voltage V_π is 1.4 V with 30 dB of ER [Fig. 3(b)]; such a modulator allows direct driving by a CMOS circuit. The EO response for a 5-mm device reaches 100 GHz [Fig. 3(c)] and the modulators in Ref. 54 were also evaluated in various ultra-high data rates including 100 Gbps on-off keying (OOK) and multi-level formats (70 GBd 4-ASK, 70 GBd 8-ASK). Other modulator configurations are also possible, such as those exploiting a ring or racetrack resonator.⁵⁵ Lithium-niobate waveguides of rib type were etched on a LN thin film of 600 nm to form the rings/racetracks and electrode pairs were defined for applying the modulation signal [Fig. 3(d,e,f)]. EO tuning efficiencies were around 7 pm/V and the 3-dB EO bandwidth was found to be 30 GHz for racetracks, limited by the cavity-photon lifetime; successful high-speed data operation was confirmed up to 40 Gbps. For a detailed theoretical analysis of the EO limitations in the performance of LN microring resonators, see Ref. 59.

Recently there have also been experimental demonstrations of devices bridging the two categories discussed in the preceding paragraphs,^{60,61} as they combine a silicon-photonic layer together with an etched LN thin film: this approach, though classified as hybrid Si-LN, is differentiated over previous hybrid ones with the LN thin film now being etched and vertical adiabatic couplers allowing for full light transfer between the underlying Si layer and the LN waveguides. In Ref. 61, the LN layer is a 600 nm thin film, where rib waveguides are dry-etched on top of a SOI circuit [Fig. 4(a,b,c)]. LN waveguides function as the phase modulators and the bottom SOI circuit implements the grating couplers for in/out chip coupling and

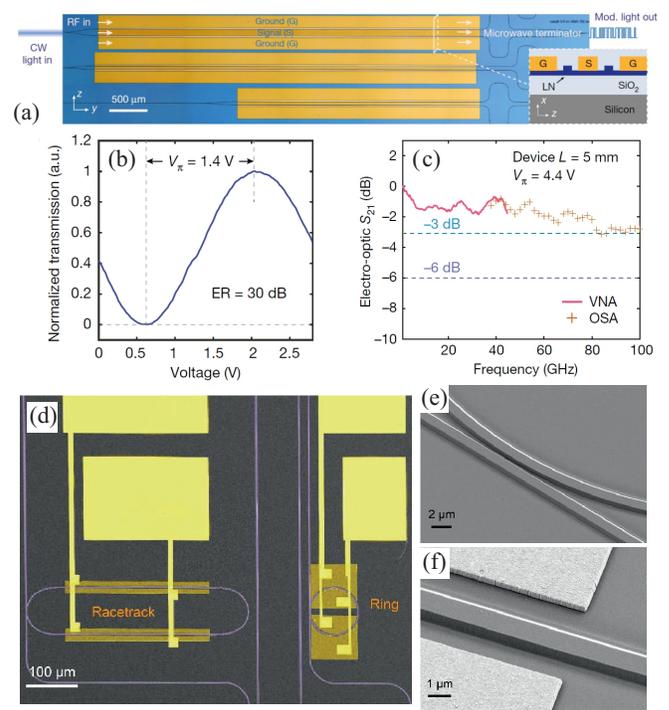


FIG. 3. Electro-optic devices based on etched single-crystal sub-micron LN thin films. (a,b,c) Monolithically integrated LN modulators in an MZI configuration.⁵⁴ Microscope image showing three MZIs with a cross-section schematic (inset). Normalized optical transmission for a 20-mm device versus applied voltage showing a V_π equal to 1.4 V and an ER of 30 dB. EO response for a 5-mm device showing a 3-dB bandwidth of 100 GHz. Adapted with permission from Ref. 54, Copyright 2018 Springer Nature. (d,e,f) EO racetrack and ring LN modulators.⁵⁵ SEM image of a racetrack and ring modulator, showing also bottom and top electrodes together with the contact pads. Close-up image of the coupling region in the racetrack resonator and close-up view of the metal electrodes with the LN rib waveguide. Adapted with permission from Ref. 55. Copyright 2018 The Optical Society.

the 3-dB multi-mode interference (MMI) couplers that form the MZI (not shown); vertical adiabatic couplers in the form of inverted silicon tapers provide the light coupling up and down between the two guiding layers [Fig. 4(d,e)]. 3-dB EO BW exceeds 70 GHz for 3-mm and 5-mm-long devices, with the former ones tested in data transmission experiments at 100 Gbps OOK and 56 GBd 4-PAM (112 Gbps) signals.

As already discussed, experimental demonstrations of devices with an EO bandwidth around 100 GHz have appeared in recent years,^{44,45,54,61} However, there are strong indications that substantially higher bandwidths are feasible⁶² that can exceed 500 GHz as estimated in Ref. 53. Support of data rates even above 1 Tbps is possible with advanced modulation formats (64-QAM at 200 GBd).⁵⁴ Although most LN modulators follow the Mach-Zehnder layout, other alternatives such as the Michelson interferometer^{60,63} have been assessed in the hybrid Si-LN platform.⁶¹ A Michelson interferometer outperforms the corresponding MZI in terms of $V_\pi L$ by a factor of two but it presents appreciably lower BW, which is mainly

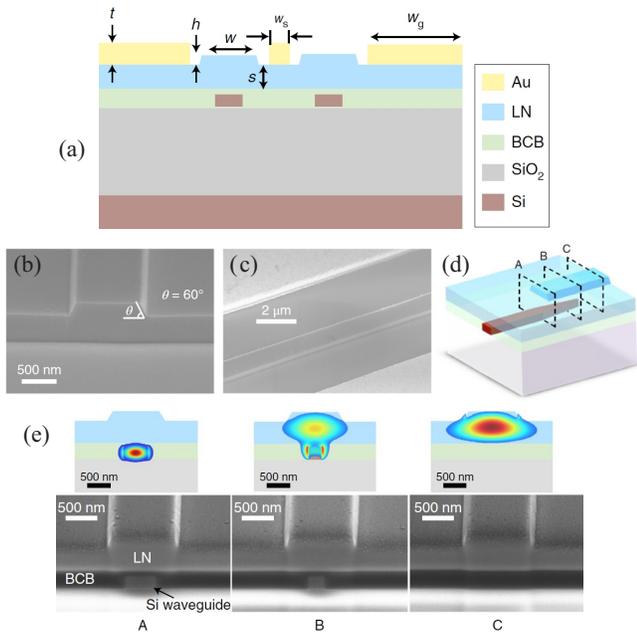


FIG. 4. Hybrid Si-LN EO Mach-Zehnder modulator with etched LN thin film.⁶¹ (a) Schematic of the device cross-section. (b) SEM image of the LN waveguide and (c) SEM image of the metal electrodes and the optical guide. (d) Schematic of the vertical adiabatic coupler that provides light transfer between the silicon layer and the LN waveguides. (e) Calculated mode distributions and SEM images at different positions along the vertical adiabatic couplers. Adapted with permission from Ref. 61, Copyright 2019 Springer Nature.

attributed to the velocity mismatch between the RF signal and the counter-propagating optical wave reflected from the MMI-based Sagnac loop mirror. Plasmonic EO modulators based on LN substrates are also possible. In a very recent publication,⁶⁴ a plasmonic directional coupler was made by two metal stripes on an un-etched LN substrate, serving both as plasmonic waveguides and signal electrodes. The device length was around $15 \mu\text{m}$ and resulted in a record-low $V_\pi L$ value of 0.21 V cm , with a theoretically estimated modulation BW in excess of 800 GHz .

The performance of LN modulators is strongly tied to the RF circuitry delivering the modulation signal. Reduced overlap between electrical and optical modes together with inefficient microwave signal delivery are identified as the main limiting factors in performance. In the case of LN modulators, traveling wave electrodes (TWEs) are the default choice, typically in the form of a coplanar waveguide (CPW)^{49,53,54,65,66} in a ground-signal-ground (GSG) configuration. Electrodes are commonly gold electroplated but also thin deposited aluminum electrodes are used in CMOS compatible devices. Three main prerequisites should be met for the RF circuitry:^{62,67} (i) minimum mismatch between the optical mode group velocity and the RF wave, (ii) the characteristic impedance of the transmission line should match the source and load impedance (typically 50Ω), and (iii) RF losses should also be as low as possible. To better appreciate the various restrictions related to the RF circuitry that

delivers the modulation signal, we quote the low-frequency relative permittivities of LN, $\epsilon_{xx}^T = \epsilon_{yy}^T = 84.5$, $\epsilon_{zz}^T = 27.8$ (free or unclamped) and $\epsilon_{xx}^S = \epsilon_{yy}^S = 45.5$, $\epsilon_{zz}^S = 26.2$ (rigid or clamped).⁶⁸ Thus, the dielectric permittivity of LN at RF frequencies is much higher than the value presented at optical frequencies ($\epsilon_{\text{opt}} \sim 4.6\text{-}5$). This usually necessitates to additionally incorporate a material with lower dielectric permittivity that will accommodate part of the RF mode and will consequently increase the RF velocity. Successful strategies to this end include tall electrodes to pull the RF electric field in air,⁶⁵ placing an SiO_2 buffer layer between the electrodes and LN,⁶⁵ extra mechanical LN thinning,⁶⁹ tuning the thickness of the oxide layer (SiO_2) underneath the LN device layer,⁵⁴ and silicon layer thinning.⁴⁵ In cases of LN thin-film modulators where $n_{\text{eff,RF}}$ is lower compared to the $n_{\text{eff,opt}}$, a higher index cladding material such as UV15 can be used⁵³ to increase $n_{\text{eff,RF}}$. Though V_π can be traded-off with the device length, longer devices present lower bandwidths as the mismatch between the optical and microwave velocities is intensified, apart from the obviously higher losses.

For devices with EO bandwidth approaching or exceeding 100 GHz ^{44,45,53,54,61,69} length is restricted to values below 5 mm , leading to increased driving voltages. For such high bandwidth, the RF line of choice is still the CPW with metal thickness in the range of $1\text{-}2 \mu\text{m}$. RF losses include the contribution of the conductor and substrate as well as radiation and reported experimental values are around $7\text{-}8 \text{ dB/cm}$ at 100 GHz ,^{44,45,61} aligned with theoretical predictions. CPW radiation losses are expected to dominate above 200 GHz . Maintaining the characteristic impedance close to 50Ω in such an extended frequency range has been proven possible, with a variation of few percent around the nominal value. Despite the technical challenges and the high material dispersion of LN, neither RF losses nor velocity matching turn out as fundamental limitations for accessing bandwidths above 100 GHz .

B. III-V Semiconductors

The family of III-V semiconductors includes compounds between group-III (Al, Ga, In) and group-V (N, P, As, Sb) elements of the periodic table. The most important representatives are the binary compounds GaAs, InP, GaP, and GaN as well as the ternary InGaAs, InGaP and the quaternary InGaAsP, InGaAlP semiconductors. The EO effect in III-V semiconductors is the resultant of multiple optical processes, including the linear EO effect, the Franz-Keldysh effect in bulk semiconductors or the QCSE in quantum-confined structures, as well as free-carrier effects. All effects contribute to changes in both real and imaginary parts of the optical permittivity, but they can often have a predominant electro-refractive or electro-absorptive nature by engineering the material and system parameters.

The linear EO property stems from the non-centrosymmetric crystal structure of III-V semiconductors, which renders the crystal polarizable, giving rise to a field-induced birefringence in addition to a piezoelectric

effect. The change in the refractive index depends in sign and magnitude on the crystal orientation, the RF field, and the light polarization. For the standard (100) cut and an RF field parallel to the surface vector, the linear EO effect becomes maximum for a light polarization parallel to [011], modulating only the TE-polarized light. The change in the refractive index is given by $|\Delta n| = (1/2)n_0^3 r_{41} E$, where n_0 is the ordinary refractive index, r_{41} the EO tensor component, and E the RF field. For GaAs, it is $r_{41} \sim 1.6$ pm/V,^{28,70} which is considerably weaker compared to the EO coefficient in LiNbO₃ (Section III A). Nevertheless, the higher refractive index of III-V semiconductors (~ 3) can partially compensate for the weaker EO coefficient, while their lower dielectric constant (~ 12) also allows for easier phase-matching in traveling-wave designs. Early studies have already shown that GaAs modulators can be highly competitive to conventional LN designs.⁷¹

The Franz-Keldysh effect manifests in bulk III-V semiconductors as changes in the absorption of near band-edge wavelengths under the application of an electric field due to the broadening and red-shifting of the absorption edge. The induced refractive-index change scales with the square of the applied field and thus constitutes a quadratic EO effect. For near band-edge wavelengths, the effect is strong and surpasses the linear EO effect, diminishing for longer wavelengths. Operation at the desired wavelength is limited by the maximum allowed field value and the absorption at the unbiased (“transparent”) state, which is in fact non-zero even for photon energies below the bandgap, despite the absence of an external electric field, due to the exponential decay of the absorption coefficient (Urbach tail) for wavelengths below the band edge. The latter is caused by deviations from the model of an ideal crystal lattice due to, e.g., defects. Bandgap engineering is frequently employed for tuning operation at the desired wavelength. The effect can be enhanced through the QCSE in quantum-confined structures.

Free-carrier effects in doped III-V semiconductors can also contribute to the modulation effect. An accumulation of free carriers increases the effective bandgap when the conduction and/or valence bands are partially filled (bandfilling effect), blueshifting the absorption edge. Such changes impact both the real and imaginary part of the complex refractive index.³⁰ The influence of electrons is particularly strong due to their smaller effective mass, while holes exhibit a much weaker EO effect, with a larger contribution to the optical loss. Suitably designing the waveguide doping profile can enhance the impact of free-carrier effects on the total index change.

Under specific conditions, all effects can contribute constructively to the aggregate EO effect. Specifically, the linear EO effect results in a positive change to the refractive index when the waveguide axis is perpendicular to the (011) facet. A positive change is also evidenced due to the quadratic EO effect, independently of the mode polarization or crystal orientation. An additional index rise can be induced in case of a carrier depletion.

Simple rib or ridge waveguides as well as buried-heterostructure formations with a width between 1.5 and 2 μm are employed for the III-V-based modulators. The RF field is

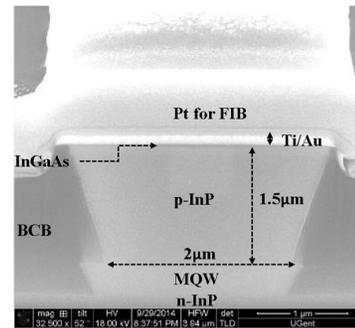


FIG. 5. SEM picture of the InP-based p-i-n junction in a MQW electro-absorption modulator.⁷² Adapted with permission from Ref. 72. Copyright 2015 The Optical Society.

usually applied through a p-i-n junction and the optical mode is guided in the high-index intrinsic semiconductor region. The dopant concentrations in the p-i-n diode are a trade-off between optical and RF losses. A strong optical confinement (large guiding layer) allows for a greater overlap with the RF electric field, lower waveguide capacitance per unit length, and reduced RF losses. The optimal design should provide a balance between high EO overlap and high RF-field intensity, while avoiding a detrimental increase in the capacitance.

The predominant representative is the electro-absorption modulator (EAM), typically employing the QCSE in quantum-confined structures to modulate the light intensity at near band-edge wavelengths. The most popular choice is the MQW structure (Fig. 5), which exhibits superior modulation properties compared to quantum dots, but also higher chirp values. A compromise is sought by alternative structures such as quantum dashes.⁷³ EAMs are simple in-line structures, usually integrated with distributed feedback lasers (DFBs), butt-coupled on the same chip, to form electro-absorption modulated lasers (EMLs). EAMs have demonstrated bandwidth values exceeding 40 GHz for driving voltages as low as a few volts and modulation lengths in the order of a hundred of microns.^{74–76} The short modulation length results in lumped-electrode modulators with an RC-limited bandwidth. The latter is in a competing relation with ER, which rises for longer devices at the expense of higher capacitance and increased ILs. A compromise is usually achieved by enhancing the modulation effect through, e.g., increasing the quantum confinement or the number of quantum wells. The latter also decreases the p-i-n capacitance and lowers the necessary bias for achieving the targeted ER, but still with a penalty on IL. Note that maintaining low ILs is important for the efficiency of the modulator and for ensuring high values of the optical modulation amplitude (OMA) to facilitate detection at the receiver. In Ref. 77, the modulation effect (Franz-Keldysh effect) is enhanced by embedding the InGaAsP waveguide in an InP photonic-crystal platform. In this approach, EAMs as long as 100 μm are demonstrated with modulation rates up to 56 Gbps (NRZ-OOK) and driving voltages below 1 V. The employed air-bridge structure provides a capacitance reduction and the energy consumption is calculated below 2 fJ/bit. Lumped-electrode EAMs have demonstrated bit rates exceed-

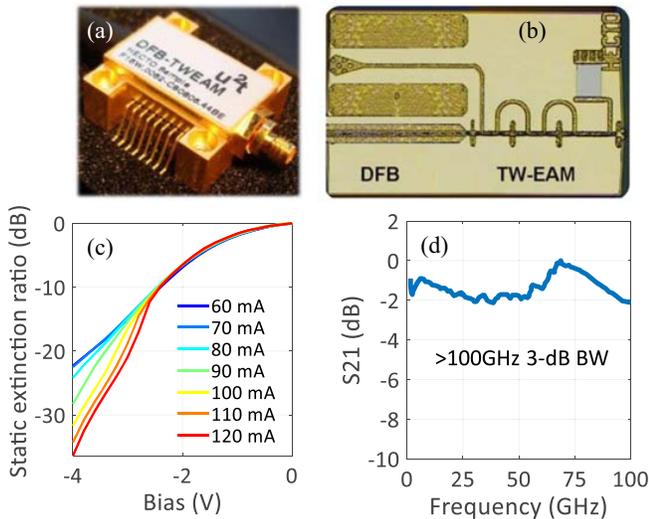


FIG. 6. Traveling-wave electro-absorption modulator.^{82,83} (a) Picture of the packaged transmitter (DFB-TWEAM) module. (b) Microphotograph of the chip ($1 \times 0.5 \text{ mm}^2$). (c) Static extinction ratio as a function of the bias voltage for varying driving currents of the DFB. (d) Measurement of the 3-dB EO BW of the TWEAM. Adapted, with permission, from Ref. 83. Copyright 2019 IEEE.

ing 100 Gbps using 4-PAM modulation formats.^{78,79} A bit rate up to 200 Gbps (4-PAM) is demonstrated in Ref. 80, where a flip-chip interconnection technique between the RF circuit and the modulator allows for a higher EO bandwidth ($> 59 \text{ GHz}$). A 300 Gbps rate has also been achieved using the DMT modulation format and a digital-preprocessed analog-multiplexed digital-to-analog converter, with the bandwidth of the employed EMLs exceeding 55 GHz.⁸¹

The length of EAMs can be a limiting factor when driven by a $50\text{-}\Omega$ system because it is translated into low impedance values. Despite the suitability of the latter for high speeds, it poses limitations at lower frequencies due to increased RF reflection. Towards achieving a better impedance match throughout the bandwidth, traveling-wave electro-absorption modulators (TWEAMs) were suggested. Through the use of segmented structures, they manage to present a higher input impedance and thus effectively suppress reflections. In Ref. 82, a 100-GHz TWEAM has been reported, with a total length of $180 \mu\text{m}$. The performance of the complete DFB-TWEAM module is shown in Fig. 6.⁸³ At a 100-Gbps (NRZ-OOK) bit rate, a dynamic ER of 4.2 dB is reported for a 2-V swing. Subsequent works based on this modulator demonstrated rates from 100-Gbps (NRZ-OOK)^{83,84} up to 200 Gbps (DMT),⁸⁵ with a potential for even higher bit rates using more complex modulation formats.

Mach-Zehnder modulators have also been demonstrated, requiring a compromise between index change and absorption to ensure short and low-loss phase shifters. Differentially driven, short, lumped electrodes result in compact 10-Gbps OOK MZMs, driven by voltages below 3 V and exhibiting a negative chirp.⁸⁶ Higher bit rates require traveling-wave electrodes, which are configured either as independent, coplanar microstrips for each interferometric arm or as a se-

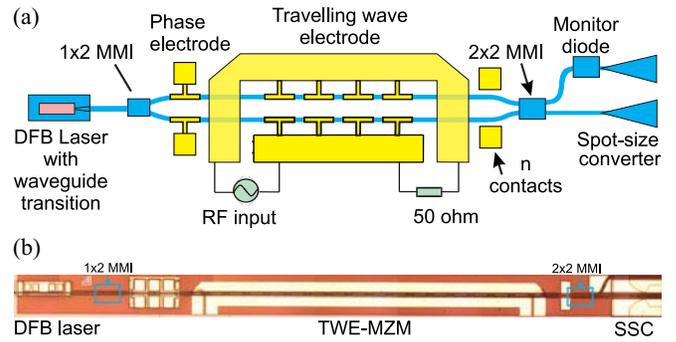


FIG. 7. (a) Schematic structure of the transmitter in Refs. 88 and 89, employing a capacitance-loaded traveling-wave-electrode MZM. (b) Photograph of the fabricated DFB-MZM module ($0.5 \times 8 \text{ mm}^2$). Adapted, with permission, from Ref. 88. Copyright 2016 IEEE.

ries push-pull configuration with a capacitance-loaded coplanar stripline electrode [Fig. 7(a)]. The latter design allows additionally for a reduction in the overlap between the RF mode and the doped regions of the p-i-n diode, reducing the RF loss, while allowing for controlling the velocity of the RF signal through changes in the distributed capacitance. Impedance matching to the typical 50Ω value is generally challenging for InP-based MZMs due to the large capacitance per unit length introduced by the p-i-n waveguide structure. A 20-GHz TWE-MZM with a 25-dB ER has been demonstrated,⁸⁷ allowing for a bit rate exceeding 40 Gbps (DQPSK) for a $3V_{pp}$ driving voltage. The more recent capacitance-loaded series push-pull TWE configuration⁸⁸ in Fig. 7 exhibits an enhanced bandwidth of 54 GHz, achieving bit rates up to 300 Gbps (8-PAM) for 0.57 pJ/bit .⁸⁹ The modulator has a static ER equal to 25 dB, IL = 6 dB, and $V_{\pi}L = 0.55 \text{ V cm}$ ($V_{\pi} = 2 \text{ V}$). In Ref. 90, the use of high-speed SiGe driving electronics allowed for a 2-channel (TE and TM) bit rate of 1 Tbps using a 100-GBd 32-QAM InP-based TWE-MZM. The use of an n-i-p-n heterostructure in Ref. 91 instead of the conventional p-i-n diode reduces the RF losses of the InP-based, capacitance-loaded TWE-MZM at high frequencies, providing an 80-GHz EO bandwidth. A static 25-dB ER is demonstrated for $V_{\pi} = 1.5 \text{ V}$ and a 4-mm length with an on-chip 8.5-dB IL. Modulation performance up to 128 GBd (QPSK) is reported with the potential for even higher rates (333 Gbps, DMT).⁹² Apart from the InP system, GaAs-based MZMs employing the GaAs/AlGaAs heterostructure have also been reported⁹³⁻⁹⁵ due to their lower cost, superior electrical properties, and temperature stability. The large offset of standard telecom wavelengths from the band-edge wavelength of GaAs ($\sim 880 \text{ nm}$) renders the weak Pockels effect as the primary modulation effect. Despite their broadband performance, they require mm-scale interaction lengths in order to maintain low driving voltages ($V_{\pi} \sim 3 \text{ V}$). ER values exceeding 20 dB have been reported, with typical fiber-to-fiber loss $\sim 7\text{-}8 \text{ dB}$. Bandwidth values in the range 25-30 GHz have been reported, demonstrating rates from 44.6 Gbps (DQPSK)⁹³ to 150 Gbps (64-QAM).⁹⁴

The high efficiency of the III-V-based modulators as well as their opportunity to be integrated with optical gain devices

(e.g., lasers, amplifiers) and high-speed electronics would be highly desirable on SOI-based platforms due to the low cost, index contrast, detection efficiency, and CMOS compatibility of the latter. To bridge the two technologies, a hybrid III-V silicon-photonics platform⁹⁶ has been introduced by bonding a III-V wafer (usually InP) on top of a fully processed SOI wafer using molecular or divinylsiloxane–benzocyclobutene (DVS–BCB) adhesive wafer bonding. The coupling between the hybrid and silicon-photonics circuitry is achieved by tapering the III-V layer. Lumped^{72,97} and TWEAM⁹⁸ as well as Mach-Zehnder modulators⁹⁹ can be formed on the III-V layer, supporting bandwidth values exceeding 67 GHz⁹⁸ and rates up to 100 Gbps (duobinary modulation).⁹⁷ Finally, III-V/Si modulators employing hybrid III-V/Si MOS capacitors have also been reported.^{100–102} The large carrier-induced index change, the high electron mobility, and low plasma absorption of III-V semiconductors are beneficial for low-loss and compact phase shifters, which outperform their pure silicon counterparts (Section III C).

C. Silicon

Silicon is undoubtedly the most popular material in integrated nanophotonics, mostly due to its massive involvement in electronics, which gave Si an important fabrication-related head start compared to other materials considered for optical integration. As a result, SOI-based modulators are nowadays one of the most studied and well-developed component in photonics. Based on the seminal works by Soref and co-workers,^{32,103} pure silicon modulators became possible by exploiting the free-carriers (plasma dispersion) effect, i.e., a small alteration in the refractive index of silicon ($\Delta n_{\text{Si}} \sim 3\%$ at most), induced by (electrically controlled) modifications in its free-carrier density. From the first experimental demonstration of an Si-based GHz modulator in 2004,¹⁰⁴ significant progress has been witnessed in modulator designs with high EO bandwidth (up to 50 GHz) and low energy consumption (< 10 fJ/bit). Transmission rates up to several hundred Gbps have been reported by resorting to advanced modulation IQ schemes.^{105–108}

Regarding the dynamic control of free carriers in silicon, various configurations have been explored over the years. These configurations include capacitor-like structures for carrier accumulation and appropriately biased p-n/p-i-n junctions for carrier depletion/injection. Carrier accumulation schemes employ a thin insulator layer (typically below 10 nm) sandwiched between Si to form a capacitor (silicon-insulator-silicon capacitor, SISCAP) so that opposite charge carriers accumulate on each side of the insulator when an external bias is applied, modifying the properties of Si. Generally, highly doped silicon is preferred to lower the series resistance, but it is often the intrinsically high capacitance of SISCAPs that can be the limiting factor regarding EO bandwidth. Despite the potential limitations, high-speed operation with moderate additional (carrier-related) losses are the main characteristics of carrier accumulation configurations, as reported in the literature.¹⁵ Finally, SISCAPs typically involve a challeng-

ing fabrication process requiring a polysilicon layer above the oxide which brings additional losses because it needs higher doping to increase modulation efficiency. Although recrystallizing is possible, this approach significantly deviates from standard commercialized CMOS techniques.¹⁵

Carrier depletion (injection) on the other hand relies on a reverse- (forward-) biased p-n or p-i-n junction to provide carrier inversion (current flow); such junctions are fabricated with standardized and CMOS-compatible processes. Carrier depletion devices are generally characterized by high speed but also high voltage-length product; nonetheless, a number of design alternatives have been developed over the years to allow for more efficient carrier-concentration control, including horizontal, vertical, interchanged, interleaved, asymmetric, and profiled junctions.¹⁰⁹ On the contrary, the carrier-injection mechanism is more efficient due to the large diffusion capacitance and the carrier-light wave interaction taking place in larger areas at the expense of a speed reduction, which is mostly limited by carrier diffusion.¹¹⁰

The first GHz-scale modulator in silicon was demonstrated in an MZI SISCAP configuration.¹⁰⁴ Although its performance metrics are considered quite poor by today's standards (approximately 3-GHz EO bandwidth and 8 Vcm voltage-length product), it paved the way for the research that followed. Within the next year, the same group achieved a significant improvement of the phase shifter,¹¹¹ measuring a $V_{\pi}L = 1.4$ Vcm. However, the interest in carrier-accumulation devices quickly started to weaken despite their promising prospects. The interest shifted to the more fabrication-friendly carrier depletion/injection configurations, with forward-biased p-i-n¹¹⁰ and reverse-biased p-n/p-i-n (vertical¹¹² or horizontal^{113–116}) junctions. In Ref. 114, a 340×450 nm² Si-slab waveguide (80-nm slab thickness) is used to configure 750- μm -long symmetric phase shifters, placed in both arms of an MZI [Fig. 8(a)]. A 50-nm horizontal offset of the p-n junction is introduced into the waveguide core [close-up view in Fig. 8(a)] to maximize the modulation efficiency; a standard practice that increases the overlap between the optical mode and the spatial free-carrier distribution. Applying a higher static reverse-bias to the phase shifters allows for higher electrical bandwidth due to a reduction in the capacitance of the p-n/p-i-n junction (and thus the RC-constant) as a result of the increase in its space charge region, but also results in higher voltage-length products [Figs. 8(b,c)]. Ultimately, a 28-GHz EO bandwidth is achieved with the demonstration of a 60 Gbps transmission rate (OOK) and a 3.6 dB dynamic ER; similar metrics have been reported in the literature for other reverse-biased junction configurations.^{106,112,117} Alternatively, forward-biased p-i-n junctions [Fig. 8(d)] provide lower voltage-length products but require (passive) equalizing RF circuits, which mostly lower the equivalent electrical capacitance to allow for comparably high-speed operation at the expense of modulation efficiency: In Ref. 110, a $V_{\pi}L = 0.62$ Vcm (60- μm -long device) is reported with a 17-GHz EO BW and a 2.1-dB dynamic ER at a 25-Gbps transmission rate (OOK).

In parallel with the development of MZI modulators, ring/disk cavities were also examined as possible candidates

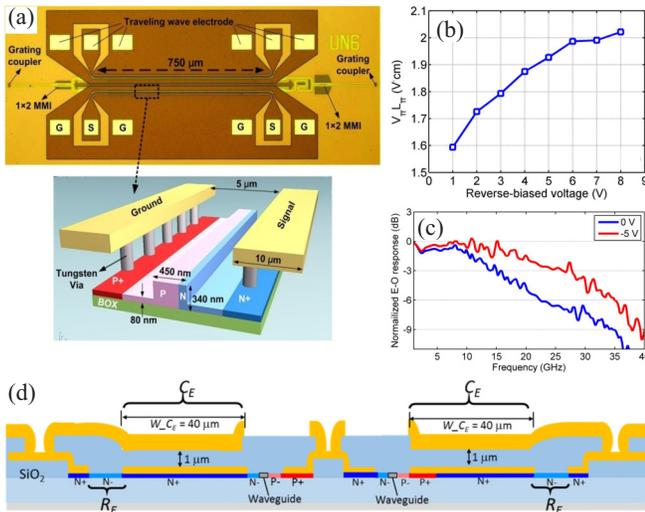


FIG. 8. Silicon MZMs. (a) Reverse-biased, horizontal p-n junction silicon modulator with its TW electrodes.¹¹⁴ (b) Dependence of voltage-length product and (c) EO bandwidth on the applied reverse bias (and thus on the carrier depletion level). Adapted with permission from Ref. 114. Copyright 2013 The Optical Society. (d) Schematic of a forward-biased p-i-n junction MZM, showing in detail the design of the equalizing electrodes (C_E and R_E regions), which increase the EO bandwidth.¹¹⁰ Adapted with permission from Ref. 110. Copyright 2015 The Optical Society.

to significantly reduce the device footprint. This comes at the expense of optical bandwidth since cavities with high quality factors (preferred for their increased pm/V tunability) are characterized by high cavity-photon lifetimes. The first demonstration of a silicon-ring modulator with a forward-biased p-i-n junction was reported in 2005 (shortly after the first high-speed silicon MZI demonstration) at a 1.5 Gbps transmission rate.¹¹⁸ Soon, the different junction alternatives discussed above were adopted in ring cavities, leading to important improvements in the measured data rates and extinction ratios. Additionally, a significant part of the research was dedicated to the efficient thermal stabilization of the cavities, constituting nowadays the main energy-consumption mechanism. In Ref. 119, the fabricated 7.5- μm -radius ring (Fig. 9) has a horizontally shifted p-n junction in the guiding area, achieving a 14.2 pm/V resonance-frequency shift, translated into a 6.2 dB dynamic ER under 40 Gbps transmission rate (OOK) with 4.8 V peak-to-peak RF signal and moderate static (-2.6 V) reverse bias. SISCAP configurations also allow for similar performance, as demonstrated in Ref. 120 for a 2.4- μm -radius disk incorporating a vertical capacitor configuration (Fig. 10). An 8 dB dynamic ER at 44 Gbps transmission rate (OOK) with 2.2 V_{pp} RF signal and only -0.5 V static reverse bias was shown. Notably, the reported resonance-frequency shift per Volt reached 250 pm/V, an order of magnitude larger than configurations relying on carrier-depletion/injection mechanisms.^{121,122}

Recently, more sophisticated modulation schemes were introduced to allow for a more efficient bandwidth usage. In ring cavities for example, 4 or even 8 levels of driving volt-

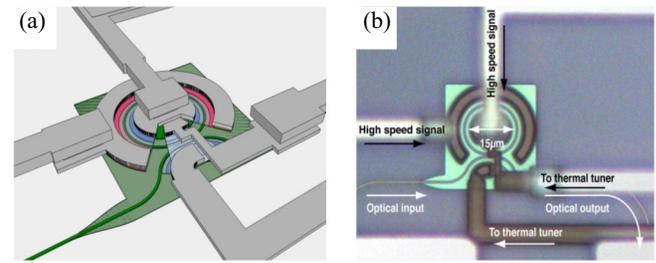


FIG. 9. (a) Schematic and (b) practical realization of silicon ring/disk modulator with a horizontal p-n junction.¹¹⁹ The electrical and thermal tuning electrodes are depicted. Adapted with permission from Ref. 119. Copyright 2015 The Optical Society.

ages were used to introduce 4-PAM and 8-PAM modulations, respectively. Such approaches led to a significant data-rate increase, with up to 160 Gbps transmission rates being reported in the literature.^{108,122,123} MZMs on the other hand, exhibit more flexibility in IQ modulation schemes, which can be realized using nested interferometers in each parent branch or by appropriately driven cascaded phase shifters.¹⁵ Although such schemes require more complex electrical circuits, single- or double-polarization 4-PAM,¹⁰⁶ QPSK,¹⁰⁵ and 16-QAM^{107,124} modulations have been successfully demonstrated, with the reported symbol rates typically reaching 50 GBd, resulting in data rates up to 240 Gbps for single-polarization¹⁰⁷ or 320 Gbps for dual-polarization 16-QAM.¹²⁴

Other alternatives have also been considered in Si modulators, such as slow-wave structures which increase the phase-shifting efficiency due to the reduced group velocity of the guided light; works on Bragg gratings and photonic-crystal structures report notable metrics.^{121,125,126} Nevertheless, the increased footprint and the relatively challenging design and fabrication processes limited the initial interest. A different alternative, systematically examined in the past few years, concerns the forced introduction of second order nonlinearity (Pockels effect) in Si. Being centrocymetric by nature, $\chi^{(2)}$ nonlinearities in silicon are introduced by applying a mechanical strain on the crystal which breaks its symmetry, typ-

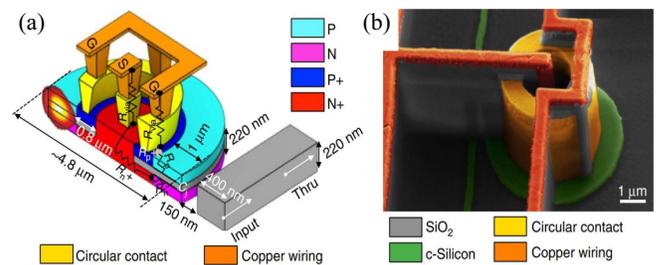


FIG. 10. Silicon ring/disk modulator. (a) Schematic and (b) practical realization of a SISCAP-based disk resonator with a vertical capacitor configuration. The mode overlap with the carrier-accumulation region is marked. Reproduced from Ref. 120 under CC BY-NC-SA 3.0 License (<http://creativecommons.org/licenses/by-nc-sa/3.0/>). Copyright 2014, The Authors, published by Springer Nature.

ically by using a silicon nitride cladding and exploiting the introduced lattice mismatch.¹²⁷ Simulations indicate the introduction of a maximum $\chi^{(2)}$ of a few pm/V but unevenly distributed in the Si core.¹²⁸ Practical realization of such modulators is in its early stages and shows moderate potential.¹²⁹ Alternatively, the Pockels effect can be introduced through the integration of silicon with linear EO materials such as lithium niobate (Section III A) or EO polymers (Section III D).

Similarly, the lack of electro-absorption effects (Franz-Keldysh effect and QCSE) in silicon can be overcome through integration with III-V semiconductors, as already discussed in Section III B. Due to its indirect (direct) bandgap of 1.12 eV (3.4 eV), electro-absorption at the NIR regime is principally not supported in silicon. In contrast, germanium, the second most popular group-IV semiconductor, exhibits, along with its fundamental indirect bandgap of 0.66 eV, a direct bandgap of 0.8 eV, which corresponds to telecom wavelengths and could be modulated for tunable absorption. The easy integration of germanium with silicon, already exploited in electronics, is a major advantage compared to the challenging III-V-on-silicon integration. EAMs exploiting the Franz-Keldysh effect in bulk Ge^{130–134} and Si_{1-x}Ge_x alloys^{135–137} as well as the QCSE in Ge/SiGe quantum wells^{138–140} have been successfully demonstrated. In each case, the wavelength of optimum modulation efficiency may differ and suitable engineering is often required for adjusting to the frequently employed telecom wavelengths. The EO effect is almost exclusively induced through a reverse-biased p-i-n junction under the application of a few Volt swing and it can be comparable to that in III-V semiconductors. Insertion losses, however, appear increased due to absorption from the indirect lower-energy bandgap of Ge. The modulation length does not exceed a few tens of micrometers, allowing for sub-fJ/bit energy-consumption estimates.¹⁴⁰ Recent demonstrations have also reported EO-bandwidth values greater than 50 GHz,^{132,133,137} which have been successfully translated into bit rates as high as 200 Gbps (16QAM).¹⁴¹ Phase modulation is also possible, exploiting electro-refractive effects in SiGe layers¹⁴² and Ge/SiGe quantum wells,¹⁴³ with a $V_{\pi}L$ product equal to 0.81 V cm reported in Ref. 142 for a 4 dB/mm of propagation loss. We also note that strained SiGe has been experimentally verified to support enhanced free-carrier effects compared to silicon.¹⁴⁴

Finally, as with lithium-niobate modulators, the electrode design plays a crucial role in the BW and energy consumption of silicon modulators.^{15,109} TWEs exhibit the challenges discussed in Section III A; to overcome these issues, sophisticated designs are commonly adopted with the implementation of perpendicular metallic segments (wire- or T-shaped)¹¹⁷ to modulate the transmission line conductance/inductance and, thus, improve the phase-matching between RF and optical guided modes and, consequently, the EO bandwidth. Copper and aluminium are the most popular options for the electrodes, approaching heavily-doped silicon with carefully designed, zigzagged vias to further control resistance and capacitance.^{114,115,117} On the other hand, lumped electrodes have several advantages¹⁵ such as significantly lower power consumption, more flexibility in their design to control the induced capacitance/inductance (given that

EO BW is RF-limited), and absence of the 50 Ω impedance-matching requirement. Electrodes implemented in resonant-modulator structures are by nature lumped (small dimensions compared to the RF-signal wavelength), but there exists an interest for lumped electrodes in MZI configurations as well. This becomes possible by using, e.g., folded (meandered) waveguides¹⁰⁹ or Sagnac loops to recycle light propagation.¹⁴⁵ The use of segmented MZMs has also been suggested.^{146,147} Discretizing the long phase-shifting elements into multiple shorter segments allows each segment to be separately driven as a lumped element of reduced RC constant. Typically, each segment is driven with an identical signal, suitably delayed to account for the optical delay between the segments. As a result, the EO bandwidth appears improved at the expense of increased driver power consumption. Compared to TWE approaches, the segmented scheme avoids the requirement for impedance terminations as well as the RF losses introduced by the transmission lines.

D. Electro-Optic Polymers

Electro-optic polymers provide an instantaneous pure phase shift, exactly as in lithium niobate. The active molecules are organic chromophores¹⁴⁸ with high molecular hyperpolarizability in a chromophore-polymer guest-host material system; chromophores are typically limited to 25 wt%. Polymer deposition or filling is based on spin coating at low temperature and the EO polymer can be combined with almost any material. After deposition, the electro-optic coefficient is zero due to the random orientation of the dipolar chromophores. Poling using a DC bias at fabrication stage provides alignment of the active chromophores along the direction of the poling electric field and takes place at elevated temperatures close to their glass-transition temperature T_g ; cooling is performed while still maintaining the poling field to freeze the chromophore orientation. With this process and after poling field removal, the chromophores remain aligned and their high molecular hyperpolarizability translates into large macroscopic EO activity (r_{33}) in the material.¹⁴⁸ Polymers with r_{33} exceeding 300 pm/V are available^{149,150} and theoretical values of chromophore hyperpolarizability predict r_{33} values even above 1000 pm/V. Such EO coefficients exceed those of LN by a factor of 10 or higher. Refractive indices of most polymers in the near infrared are in the 1.5-1.7 range, with similar refractive indices exhibited at microwave frequencies, as well. This facilitates the velocity matching between the propagating optical and RF fields, when traveling-wave electrodes are used. Long-term stability of EO polymers is of importance, with materials having a higher glass-transition temperature being advantageous. Thermal stability can be improved by cross-linking performed during poling,¹⁵¹ which results in lattice hardening and increases T_g .

Early polymeric electro-optic modulators were based on MZIs made of waveguides with large cross-section (width in the range of a few microns) and low index contrast, with the EO polymer being the waveguide core material. Successful examples included ridge structures defined by reac-

tive ion etching^{152–154} or channels/trenches filled with the EO polymer.^{155–157} Popular choices for the host material were Poly(methyl methacrylate) (PMMA) and amorphous polycarbonate (APC). Poling required high, vertically applied voltages of hundreds of volts and in many prototypes poling electrodes were removed before the fabrication of the microstrip signal lines.^{153,157} For this class of devices, $V_{\pi}L$ values of a few V cm were routinely reported in experimental demonstrations together with static extinction ratios above 20 dB, thus allowing for sub-1-V operation in devices that were 2–3 cm long.^{151,155,156} Importantly, the potential for substantial EO bandwidth, well-exceeding 100 GHz, was experimentally demonstrated very early.¹⁵⁴

Alternatively, the EO polymer can be introduced as the cladding material in a silicon-core waveguide. A device characterized by fabrication simplicity was demonstrated in Ref. 158 with an ultrathin (50 nm) silicon core overlaid by polymer; for a 10-mm-long device, a V_{π} of 0.9 V was measured together with a 3-dB bandwidth of 23 GHz.

It was theoretically understood in Ref. 159 that the silicon-slot waveguide holds significant potential as the underlying structure to accommodate the EO polymer due to the high mode confinement in the slot area, in particular for narrow (sub-100 nm) slots. Shortly after, experimental demonstrations^{160,161} reported polymer-clad silicon-slot modulators with $V_{\pi}L$ below 1 V cm.

The polymer-clad silicon-slot waveguide that provides phase modulation is the fundamental building block of more complex devices that subsequently implement various modulation formats, ranging from the simplest intensity modulation (OOK) to advanced multi-level formats; this is collectively referred to as the silicon-organic hybrid (SOH) platform^{162–172} that was intensively developed for over a decade and reached high levels of performance. Fig. 11(a,b) schematically depict a representative MZM of this class, together with the optical mode profile [Fig. 11(c)] and the RF mode profile [Fig. 11(d)].¹⁷³ Significant improvement in bandwidth was made possible by the application of a DC gate voltage between the ground electrodes and the silicon substrate inducing an electron-accumulation layer in the thin Si striploads (slabs) that connect to the Si rails, thus lowering their resistance.¹⁶² Signal lines are coplanar waveguides which are advantageous as the poling voltage is efficiently applied between the two ground electrodes [Fig. 11(a)].¹⁶⁴ As shorter devices are sufficient due to the very low $V_{\pi}L$ product that falls below the limit of 1 V mm, it is also possible to operate the transmission line (CPW) without termination resulting in further suppression of energy consumption.¹⁶⁹ In this case, energy consumption is mainly attributed to the losses from charging/discharging the slot capacitor and approaches fJ/bit levels. The electro-optic activity can be further boosted by employing mixtures of two chromophores (binary-chromophore organic glasses)^{167,168} [Fig. 11(e)] or pure chromophores^{169,171} instead of chromophore-polymer guest-host systems; in Ref. 171 the $V_{\pi}L$ product dropped to 0.32 V mm [Fig. 11(f)]. Improvements in SOH platform lead to IQ modulators that can be directly driven by FPGA outputs¹⁷⁰ at voltages below 0.5 V, thus avoiding digital-to-

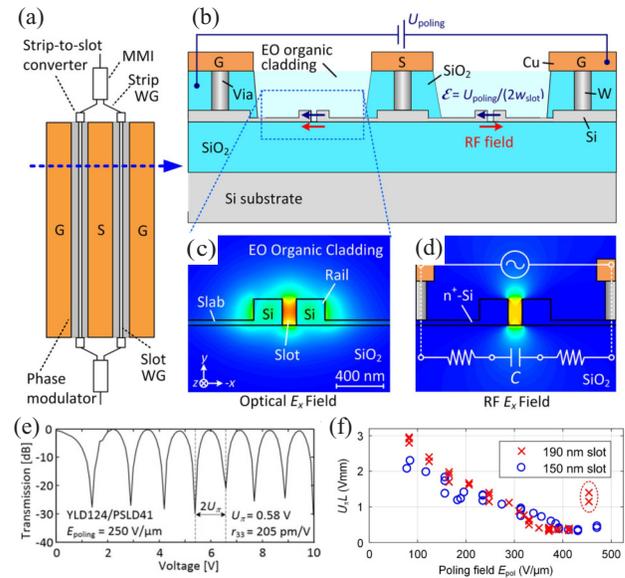


FIG. 11. Silicon-organic hybrid Mach-Zehnder modulator.¹⁷³ (a) Top-view schematic showing the slot-waveguide phase modulators and the coplanar waveguide carrying the modulation signal. (b) Cross-section showing the Si-slots connected to the CPW with tungsten vias and the direction of poling field (blue) and RF field (red). (c) Optical mode profile. (d) RF mode profile. Adapted, with permission, from Ref. 173. Copyright 2016 IEEE. (e) Experimental voltage-dependent transmission for a 1-mm-long SOH MZM when the EO cladding is the binary chromophore system YLD124/PSLD41.¹⁶⁸ Adapted, with permission, from Ref. 168. Copyright 2014 IEEE. (f) Measured $V_{\pi}L$ product for two different slot widths versus poling field.¹⁷¹ Adapted with permission from Ref. 171. Copyright 2018 The Optical Society.

analog converters or drive amplifiers. Many different modulation formats were successfully tested at different symbol rates, including OOK, 4-ASK, 8-ASK, BPSK, QPSK and 16-QAM, with the highest reported rate being 100 GBd 16-QAM (400 Gbps) in Ref. 172.

EO-polymer infiltrated photonic-crystal slot waveguides in silicon were also investigated and successfully employed as the phase-shifting elements in MZIs.^{157,174–177} For such devices, enhanced performance is anticipated due to the slow light effect close to the bandgap edge, with the effective (in-device) r_{33} acquiring very high values as intensified by the high group index n_g . Fig. 12(a) depicts an SEM image of a photonic-crystal slot waveguide (phase modulator) fed by a silicon strip waveguide through a mode converter section, Fig. 12(b) shows the band-engineered photonic-crystal lattice and the slot, Fig. 12(c) is a cross-sectional view after polymer infiltration of the slot and lattice holes have taken place, and Fig. 12(d) zooms in the slot area.¹⁷⁶ The 300-μm-long device reported in Ref. 176 achieves an average group index $n_g \sim 20$ and exhibits a $V_{\pi} \sim 0.97$ V ($V_{\pi}L = 0.29$ V mm); slightly improved performance was reported in Ref. 177 together with a measured EO BW of 15 GHz.

Plasmonic modulators exploiting EO polymers offer a possible route to further footprint shrinkage, where practical de-

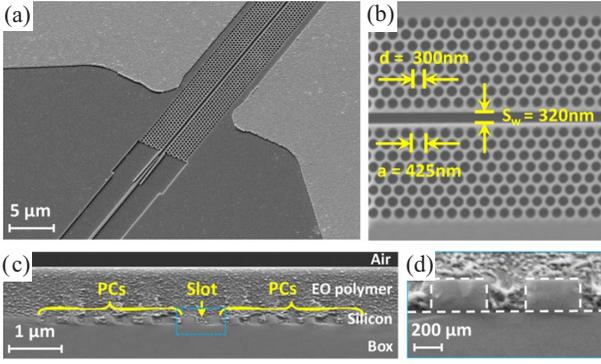


FIG. 12. Silicon photonic-crystal slot waveguide infiltrated with the EO polymer SEO125.¹⁷⁶ (a) Tilted view of the phase modulator showing part of the gold electrodes, the feeding strip waveguide and the mode converter, (b) photonic-crystal lattice and slot, (c) waveguide cross-section after EO polymer infiltration, and (d) expanded view of the blue rectangle in (c). Adapted with permission from Ref. 176. Copyright 2013 The Optical Society.

vices will be a few tens of microns long. The plasmonic slot waveguide formed by the RF electrodes is the natural choice, with almost perfect overlap between the RF field and the plasmonic mode.^{173,178,179} Bandwidth limitations related to the RC-constant are suppressed due to the presence of a highly conducting path that gives a capacitive nature to the device, leading to a theoretical EO response up to 1 THz. Transmission experiments with different rates and formats were conducted (OOK, BPSK, 4-ASK), including 100 Gbps OOK and 60 Gbd 4-PAM (120 Gbps) in devices as short as $12.5 \mu\text{m}$.¹⁷⁹

Electro-optic polymer modulators based on resonant structures, such as microrings, are another possibility. Although less studied than MZI designs, many different alternatives have been assessed, theoretically or experimentally, including: silicon nitride rings with an EO polymer top cladding,¹⁸⁰ rings written by direct photo-definition in SU8 containing chromophores,¹⁸¹ rings made of ultrathin silicon cores overlaid with an EO polymer,¹⁸² microdisks in hybrid plasmonic (conductor-gap-silicon) waveguides,¹⁸³ polymer-infiltrated sub-wavelength grating waveguide ring resonators,¹⁸⁴ and all-polymer rings.¹⁸⁵

E. Transparent Conducting Oxides

Conventionally applied to liquid crystal displays and photovoltaics,¹⁸⁶ transparent conducting oxides (TCOs) have attracted considerable interest during the last decade for NIR electro-optic applications due to their large optical tunability and the manifestation of the epsilon-near-zero (ENZ) effect. The material class of TCOs consists of degenerately doped, usually n-type, wide bandgap semiconductors.³⁴ Well-known representatives include Sn-doped indium oxide (indium tin oxide, ITO), aluminum-doped zinc oxide (AZO), and gallium-doped zinc oxide (GZO). The high concentration of their free carriers, typically between $n_e \sim 10^{19}$ - 10^{21} cm^{-3} , together with the absence of interband transitions allows for the

adoption of the Drude model for describing their NIR permittivity ϵ_{TCO} with a plasma frequency $\omega_p \propto \sqrt{n_e}$. In contrast to the ultraviolet and far-infrared plasma frequencies of metals and typical degenerately doped semiconductors, respectively, the plasma frequency of TCOs lies in the NIR regime. This renders them transparent at visible frequencies, while the high concentration of their free carriers and their appreciable mobility ($\sim 50 \text{ cm}^2/\text{Vs}$) ensure high electrical conductivity. These properties have made TCOs the predominant option for realizing transparent electrodes at visible frequencies.

The interest in TCOs has recently been renewed for applications in the field of integrated photonics. Due to their NIR plasma frequency, TCOs exhibit highly dispersive NIR properties, with the carrier-induced change in the real part of the effective mode index, $\Delta n_{\text{eff}}^{\text{Re}}$, calculated an order of magnitude greater in TCO-loaded waveguides than in pure silicon ones.¹⁸⁷ The TCO permittivity spans from dielectric to metallic values, intermediately crossing an epsilon-near-zero region with $|\epsilon_{\text{TCO}}| \approx 0$. Integrating a thin TCO film ($\sim 10 \text{ nm}$) into standard waveguide structures, allows for the manifestation of the ENZ effect for the guided mode that is perpendicularly polarized to the TCO layer. The ENZ effect consists in the significant enhancement of the electric field in the ENZ medium as a result of the boundary condition demanding the continuity of the normal component of the displacement field. The field enhancement results in increased propagation losses that scale with $\text{Im}\{\epsilon_{\text{TCO}}\}/|\epsilon_{\text{TCO}}|^2$, suggesting the potential for modulating the losses of a guided wave by changing the complex permittivity ϵ_{TCO} of the TCO layer.

The concept was first introduced in 2010 for an ITO layer sandwiched between two gold electrodes, adding an SiO_2 layer to form a MOS capacitor.¹⁸⁸ Applying an electrical bias between the gold electrodes increased the concentration of free carriers in a nm-region close to the SiO_2 -ITO interface due to the field effect, changing the plasma frequency of ITO and, thus, the value of its complex permittivity at the operation wavelength (800 nm). This altered the effective mode index n_{eff} of the plasmonic mode guided in the equivalent metal-insulator-metal (MIM) waveguide. Tuning the free-carrier concentration in ITO through external bias was suggested as a promising means of modulating the guided wave.

Since then, a plethora of TCO-based modulators has been reported at the telecommunication wavelengths of $1.31 \mu\text{m}$ and $1.55 \mu\text{m}$.³⁶ A MOS-type junction is almost exclusively selected for controlling the free-carrier concentration in TCO, with ITO being the most usual choice due to its well-studied properties as well as its low resistivity.^{189,190} A few-nm thick high- κ dielectric¹⁹¹ is usually employed for the oxide layer. Plasmonic¹⁹²⁻¹⁹⁵ or hybrid-plasmonic¹⁹⁶⁻¹⁹⁹ waveguides are commonly preferred as physical structures due to providing enhanced light-matter interaction and easy access for applying the electrical signal. Silicon-photonic designs have also been investigated,²⁰⁰⁻²⁰³ reporting reduced IL at the expense of larger footprint values. Most works study the simplest OOK modulation scheme in TCO-loaded straight waveguides, modulating the propagation loss by switching between the dielectric and the ENZ region of the TCO film. Fewer works prefer to exploit the carrier-induced changes in the real

part of TCO permittivity to investigate MZI,^{204,205} resonator-based,^{206–208} or phase modulators,^{187,209,210} capitalizing on the large changes in $\Delta n_{\text{eff}}^{\text{Re}}$ instead of the highly lossy ENZ region.

Theoretical studies on TCO-based waveguide modulators report a maximum ER of $\sim 1 \text{ dB}/\mu\text{m}$ for an optimally designed Si-slot design,²⁰³ increased to 1.5–2.5 $\text{dB}/\mu\text{m}$ for hybrid- or purely plasmonic designs.^{211,212} Regarding phase-modulation schemes, a voltage-length product of 0.15 V mm is calculated for an ITO-loaded Si-slot phase shifter.¹⁸⁷ In all cases, the required bias swing does not exceed 5 V due to the use of high- κ dielectrics, with the respective energy consumption calculated in the order of 1 pJ/bit or less for achieving a 10-dB ER or a π -phase shift. The theoretical EO BW of in-line TCO-based OOK modulators exceeds 100 GHz,^{192,195,213,214} whereas that of phase modulators is restricted to a few tens of GHz.¹⁸⁷ Depending on the modulation scheme, the required interaction length spans from a few micrometers (10-dB-ER OOK) to a few tens of micrometers (BPSK) with reference to the Si-slot platform.^{187,203} Due to the reduced footprint values of TCO-based modulators, the devices are safely considered lumped regarding the RF signal since the (electrical) length of the required electrodes is sufficiently short. As a result, no traveling-wave effects or termination issues are present, with both energy consumption and bandwidth being ultimately limited by the device capacitance and resistance values.

Recent experimental demonstrations have verified the principle of TCO-based modulators, mostly on hybrid-plasmonic^{215–217} or purely plasmonic platforms,¹⁹⁵ reporting ERs as high as 1.63 $\text{dB}/\mu\text{m}$ ²¹⁶ and 2.71 $\text{dB}/\mu\text{m}$, respectively, with a bias swing equal to 2 V. In Fig. 13(a), an SEM image of a hybrid-plasmonic Si/HfO₂/In₂O₃/Au waveguide modulator is presented.²¹⁶ Silicon-photonic implementations have also been investigated,²¹⁸ achieving at best an ER of 0.15 $\text{dB}/\mu\text{m}$ (Si-slot design) for a 12 V bias swing. A directional-coupling scheme using ITO-loaded silicon waveguides²¹⁹ has provided ER= 2 dB and IL= 2 dB for a 4 μm length and a 4 V bias. Resonator-based modulators^{206–208} have reported a maximum wavelength tunability of 271 pm/V, achieving a 15-dB ER for the ITO-loaded silicon microring in Fig. 13(b) for a 2-V bias swing. Recent MZI implementations^{204,205,220} have exploited ITO-based phase shifters, with the one in Fig. 14 achieving a markedly low $V_{\pi}L = 0.095 \text{ V mm}$, resulting in a 3-dB static ER for a 20-V bias swing.

The major drawback of the exhibited implementations is their excessive ILs as well as their limited bandwidth, which does not exceed the value of a few GHz.^{205,216,221} Both issues were attributed to low mobility TCO films and/or non-optimal design options. Despite the open research questions, the metrics of TCO-based modulators appear compatible with state-of-the-art modulation requirements, competing genuinely with the performance of other material platforms.^{204,205,214}

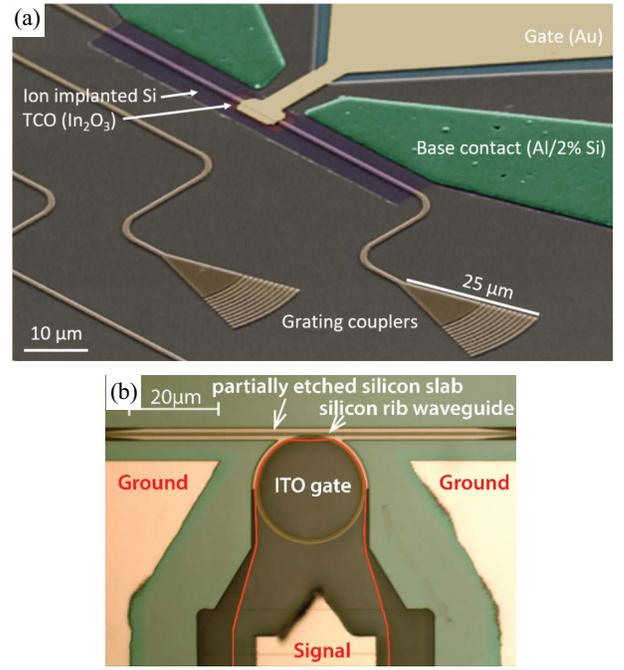


FIG. 13. (a) SEM image of the Si/HfO₂/In₂O₃/Au waveguide modulator in Ref. 216. The bias is applied between the doped Si and Au. An ER of 1.62 $\text{dB}/\mu\text{m}$ is achieved for a 6-V bias swing. A maximum rate of 2.5 Gbps is reported. Adapted with permission from Ref. 216. Copyright 2018 The Optical Society. (b) Optical image of the Si/HfO₂/ITO microring modulator (radius 12 μm) in Ref. 207. The red line highlights ITO. The ground electrodes are connected to Si through a partially etched slab. An ER of 15 dB with an IL of $\sim 5 \text{ dB}$ is reported using a 2-V bias swing. A 12-ns response time is achieved. Adapted with permission from Ref. 207. Copyright 2019 Chinese Laser Press.

F. Two-Dimensional Materials

Two-dimensional (2D) materials exhibit tunable physical properties under moderate electrical or optical stimuli. Graphene is the best-known representative, but other materials such as silicene, germanene and germanane, black phosphorus (BP), transition metal dichalcogenides (TMDs) (e.g., MoS₂, WS₂), MXenes (e.g., Ti₃CNT_x, Ti₃C₂T_x), and their combinations in Van der Waals heterostructures (e.g., graphene-BP, graphene-TMD, TMD-TMD) have gained a lot of interest lately.^{222–225} Manifesting diverse electronic band structures (from the gapless graphene to the indirect to direct bandgap transition in TMDs), 2D materials exhibit diverse macroscopic physical properties. Importantly, their high-frequency (optical) electromagnetic properties (real and imaginary part of the complex surface conductivity σ_s) are tunable under electrical stimulation and as such are considered for electro-optic (both electro-absorption and electro-refraction) modulation. Typically, the tunability is introduced by electrical gating, which modifies the carrier distribution and consequently affects the allowed electron transitions due to the Pauli blocking principle.

The first experimental demonstration of graphene-based

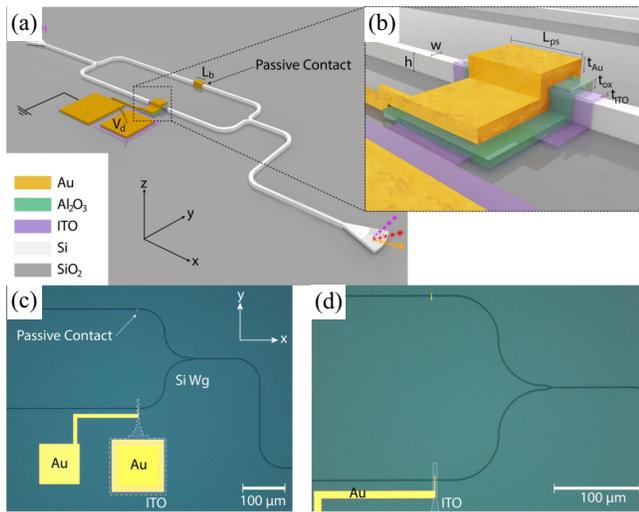


FIG. 14. (a) Schematic of an ITO-based MZM. The passive metallic contact L_b is utilized for loss balancing. (b) Close-up view of the tunable Si/ITO/Al₂O₃/Au region. (c) Optical microscope image of the modulator. The white dashes highlight the patterned ITO film. (d) Zoomed view at the device section formed by ITO (white dashes) and the Au contact, separated by Al₂O₃. The ITO-based phase shifter achieves a voltage-length product equal to 0.095 V mm. MZM exhibits a 3-dB ER, 6.7-dB IL, and a 1.1 GHz bandwidth for a 1.4- μ m phase shifter and a 20-V bias swing. Reproduced from Ref. 220 under CC BY 4.0 License (<http://creativecommons.org/licenses/by/4.0/>). Copyright 2021, The Authors, published by Springer Nature.

electro-absorption modulators dates back to 2011.^{226,227} In Ref. 226, a single-layer graphene (SLG) was overlaid on a standard 40- μ m-long silicon-rib waveguide using a thin Al₂O₃ layer for insulation, while in Ref. 227 a stack of graphene-insulator-graphene (GIG) was implemented to increase the extinction ratio over the SLG case from 0.1 to 0.16 dB/ μ m under moderate voltage (6 V). Nevertheless, the 3-dB BW was limited to 1 GHz because of the RC-constant owing to the configuration and quality of the contacts. In the following years, modulation speed was increased by improving the contact quality and positioning, resulting in an EO bandwidth up to 35 GHz,²²⁸ retaining acceptable ERs and reasonable voltage requirements using SLG or GIG structures and optimizing the graphene-light interaction^{228–230} In Ref. 229, a 5.2-dB static ER was demonstrated experimentally in a 50- μ m-long n-doped silicon-rib waveguide enhanced with an SLG on-top [Fig. 15(a,b)], operating in the fundamental TM mode. Light-graphene interactions in longer waveguide segments can lead to higher ERs at the expense of higher ILs [Fig. 15(c)] and a possible modulation speed reduction. On the other hand, in Ref. 231, a compact MIM plasmonic modulator with graphene was demonstrated; despite the relatively high ER per unit length (0.13 dB/ μ m), the reported ER was poor, in order to keep ILs reasonable (0.68 dB/ μ m). Theoretical works with hybrid-plasmonic waveguides reveal a potential for modulation^{232,233} but practical realization and experimental verification remain challenging.

Electro-refractive modulators incorporating graphene have

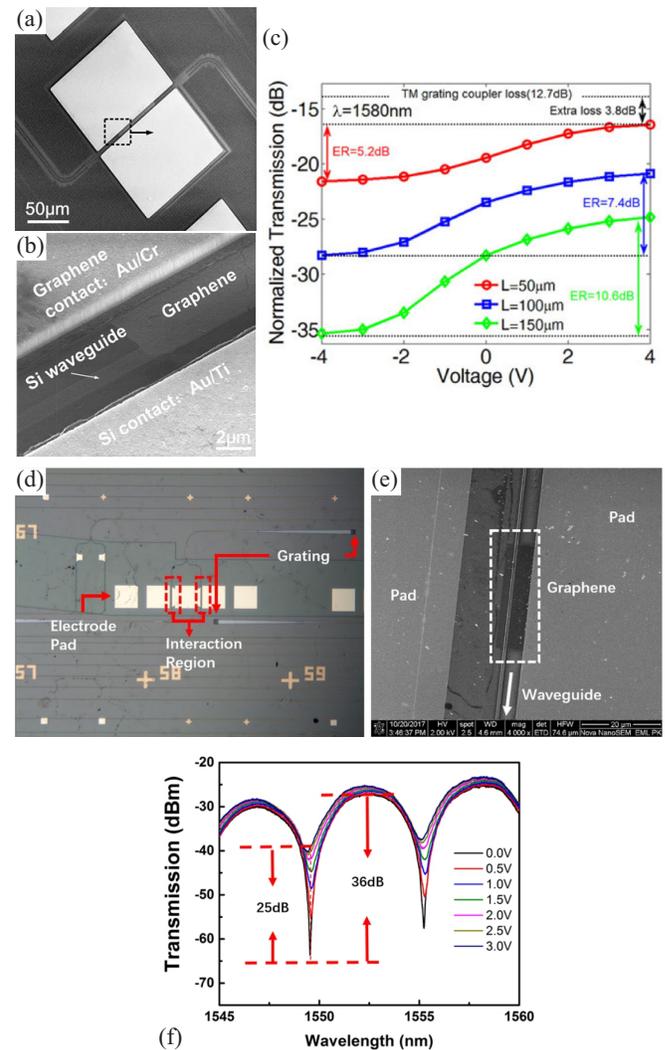


FIG. 15. Experimentally demonstrated graphene-based EAM and MZM in the silicon platform. (a,b,c) Silica-clad silicon-rib waveguide overlaid with a single graphene layer.²²⁹ A 5.2-dB extinction ratio with a 6.1-GHz BW was demonstrated. Reproduced with permission.²²⁹ Copyright 2016, Wiley-VCH. (d,e,f) Silica-clad silicon MZM. Both its arms were overlaid with an SLG. A very high static ER of 25 dB, was demonstrated. Reproduced from Ref. 234 under CC BY 4.0 License (<http://creativecommons.org/licenses/by/4.0/>). Copyright 2018, The Authors, published by Springer Nature.

also been extensively studied because of the capability to tune graphene properties under moderate voltage. The first experimental demonstration of an integrated silicon MZI overlaid with graphene showed a poor $V_{\pi}L$ of 30 V cm with high insertion loss.²³⁵ In Ref. 234, a significantly reduced $V_{\pi}L = 0.13$ V cm was demonstrated with a 25-dB static ER [Fig. 15(f)]. The MZI configuration (top view) is shown in Fig. 15(d,e) (optical and zoomed SEM images, respectively), having an SLG on top of the silica-clad silicon-rib waveguide and a thin Al₂O₃ insulating layer. Each MZI branch has a 40 μ m interaction length with graphene. A similar but larger design (300–400 μ m interaction length) was also reported in

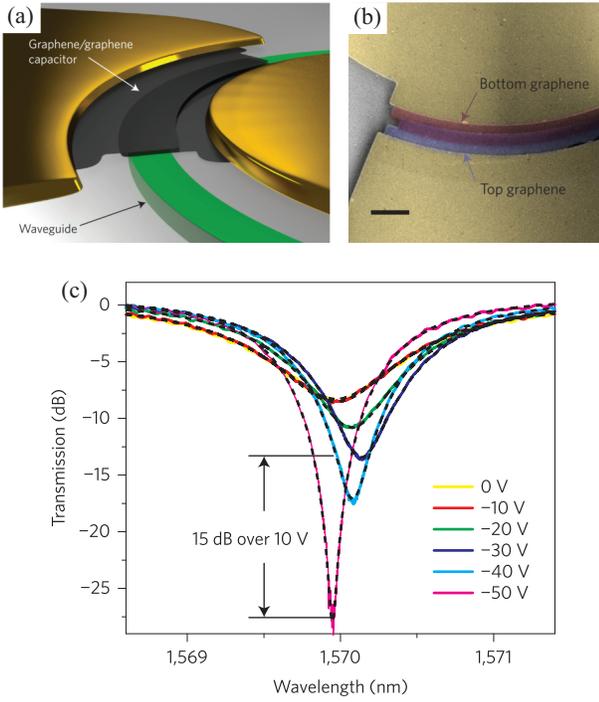


FIG. 16. A silica-clad silicon-nitride ring resonator overlaid with a GIG layer for optical modulation.²³⁷ A notable 15-dB ER over 10 V was demonstrated, topping at ~ 20 dB over 40 V, with a 30-GHz EO bandwidth. Adapted with permission from Ref. 237, Copyright 2015 Springer Nature.

the same year with a two times higher voltage-length product (0.28 V cm) and equally high static ER (35 dB), demonstrating a 5-GHz electro-optic bandwidth.²³⁶

Another possible configuration to minimize footprint (at the expense of optical bandwidth) is a ring-resonator cavity. In principle, for a cavity operating near the critical-coupling condition, tuning the properties of graphene will lead to abrupt changes in both frequency and lineshape of the resonance, allowing for high performance metrics.²¹⁴ Early experimental confirmation in a silica-clad silicon-nitride ridge waveguide with a GIG on top, covering only a fraction of the ring circumference [Fig. 16(a,b)] revealed a high static ER of 15 dB with an EO BW of 30 GHz (limited by the RC-constant and not the photon lifetime of the cavity) and acceptable insertion losses [Fig. 16(c)].²³⁷ On the contrary, a fully covered ring with an SLG layer turned out to be insufficient to induce the necessary changes in n_{eff} , resulting in a poor 3.5-dB ER,²³⁸ owing possibly to the experimental conditions (relatively low bias voltage). A few works with photonic-crystal (standing-wave) resonators have also appeared in the literature^{239,240} showing high performance (ER > 10 dB).

G. Phase-Change Materials

Phase-change materials experience a phase transition that can be driven electrically, and are being actively considered for both volatile (e.g. modulation) and nonvolatile (e.g. mem-

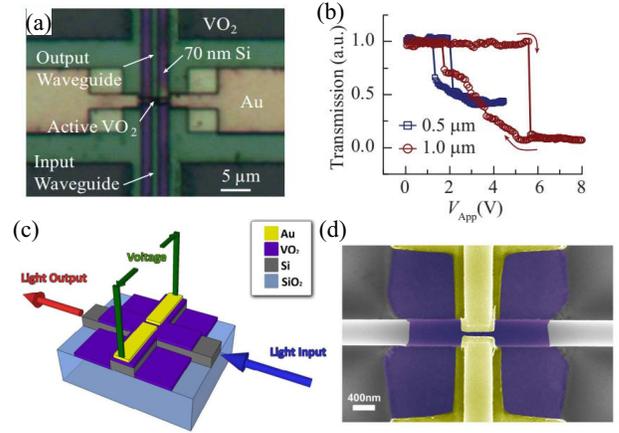


FIG. 17. Experimentally demonstrated VO₂-based modulators based on silicon-photonic waveguides. (a,b) Silicon-ridge waveguide coated with a 1 μm -long VO₂ strip.²⁴² An ER of 12 dB was experimentally demonstrated. A shorter modulator length (0.5 μm) reduces the voltage requirements at the expense of lower ER. Adapted with permission from Ref. 242. Copyright 2015 The Optical Society. (c,d) Silicon wire coated with VO₂.²⁴³ Using 10-ns voltage pulses and limiting the current passing through the device to harness only the electronically-induced transition, switching speeds as fast as 2 ns were reported. Adapted with permission from Ref. 243. Copyright 2015 American Chemical Society.

ory) electro-optic applications. In this Section we focus on the exploitation of VO₂ in integrated silicon-photonic modulators, which is the most widely investigated PCM-based modulator platform.²⁴¹

Efficient electro-optic modulators can be demonstrated by integrating VO₂ inclusions in a waveguide and exploiting the different waveguiding characteristics at the two states of VO₂ (insulating and metallic) by engineering the mode overlap with the material. In Ref. 242, an EAM with high extinction ratio reaching 12 dB has been experimentally demonstrated by coating a silicon-ridge waveguide with a strip of VO₂ which is only 1 μm long along the propagation direction [Fig. 17(a)]. The VO₂ strip can be even shorter (0.5 μm), reducing the voltage requirements for switching to the metallic state (corresponding to low transmission due to higher loss), but at the expense of lower extinction ratio [Fig. 17(b)]. A similar geometry has been studied experimentally in Ref. 243 [Fig. 17(c,d)]. By using 10-ns voltage pulses and limiting the current passing through the device to avoid thermal effects and harness only the electronically induced transition, switching speeds as fast as 2 ns were reported.²⁴³ However, in this case only a small fraction of the material was switched to the metallic state, limiting the extinction ratio to ~ 1 dB.

In recent years, several different approaches to the design of silicon-based VO₂ modulators have been theoretically explored in the literature^{244–249} (lacking, however, experimental verification) and include tailoring the position of the VO₂ inclusion in the waveguide configuration, exploiting coupled waveguides, and harnessing plasmonic effects. In Ref. 247, VO₂ is integrated in the gap of a silicon-slot waveguide to enhance mode overlap with the material and

achieve maximum disparity of the waveguiding characteristics in the two states. As a result, an ER of 21 dB is attained with a component length of 1 μm . Plasmonic waveguiding effects aiming to miniaturize the waveguide cross-section have also been exploited,^{244–246,248,249} relying on variants of hybrid silicon-plasmonic waveguide configurations (conductor-gap-silicon²⁵⁰ and symmetric/long-range conductor-gap-silicon²⁵¹ geometries). In Ref. 245, a CMOS-compatible electro-absorption modulator based on a Si/SiO₂/VO₂/Cu hybrid plasmonic waveguide has been proposed that offers extinction ratios of 3.9 dB and 8.7 dB for the fundamental TE and TM modes, respectively. Plasmonic effects in a chain of metallic disks patterned on a hybrid plasmonic waveguide have resulted in a modulator design with an ER of 9 dB/ μm .²⁴⁶ Instead of a single waveguide segment, synchronization effects between supermodes in coupled waveguide systems can be also exploited for designing modulators (as well as switching and routing) devices.^{244,248} In Ref. 244, a directional coupler scheme between a silicon waveguide and a hybrid plasmonic waveguide exploiting VO₂ as the plasmonic material was designed with an extinction ratio exceeding 3 dB in the entire C-band. However, the coupled-waveguide strategy results in an increased component length reaching 10 μm .

IV. PERFORMANCE TRENDS AND IMPORTANT DESIGN ASPECTS

A. State-of-the-art Performance and Trends

Electro-optic modulators employ either a loss- or a phase-tuning unit for modulating the guided wave. Fig. 18 compiles and compares the modulation efficiency of such units for the material platforms discussed in Section III. In each case, modulation efficiency is quantified through the appropriate metric, extracted from the respective experimentally demonstrated modulators. Beginning with loss-modulating units [Fig. 18(a)], efficiency is quantified by the ER achieved per unit length and the respective IL penalty. The values are extracted from in-line modulators in the literature, after excluding coupling losses stemming from, e.g., grating couplers and tapered sections. Loss-modulating units capitalize on the electro-absorption effect in III-V and Si-Ge platforms, as well as 2D materials. Alternatively, optical losses can be modulated by tuning the interaction between the optical electric field and free carriers in the material. This mechanism is highly efficient in TCO-based modulating units due to the field enhancement provided by the ENZ effect and it is also exploited in phase-change materials through the transition from the insulating to the metallic state. Adopting the ratio ER/IL as a metric of modulation efficiency [contour lines in Fig. 18(a)], III-V-based modulating units exhibit the highest efficiency,^{82,252} closely followed by the Si-Ge platform.¹³⁵ However, integration challenges of the former with standard silicon-photonics circuitry could render the latter more favorable. The less mature platforms comprising TCO and 2D materials exhibit a wider distribution of performance metrics, which could be attributed to varying quality of the de-

posited samples. The latter also affects the level of ILs, which could be reduced for samples with higher carrier mobility values. A thorough performance evaluation should also consider the required driving voltage, which does not exceed a few Volts in the case of III-V and Si-Ge platforms. Higher values are typically reported for TCO and 2D platforms, where the use of high- κ dielectrics is suggested as a possible solution. Electro-optic bandwidth can also be proven a limiting factor for some platforms (e.g., PCM) towards demonstrating high-speed modulators.

Next, phase-shifting units are examined in Fig. 18(b); their modulation efficiency is quantified in terms of their $V_\pi L$ product and the propagation loss a of the underlying waveguide. The metrics were extracted from literature works on phase shifters, MZMs, operating either in single-arm or push-pull configuration, and resonant modulators. To establish a common reference point, $V_\pi L$ was adjusted to indicate the required bias V_π in order for a standalone waveguide to accumulate phase π after length L . The propagation loss of the waveguide was deduced from the usually reported total IL of the modulator, subtracting contributions from grating or MMI couplers, Y-junctions, etc., to retrieve the loss introduced by the phase shifting unit itself. In case of bias-dependent propagation loss (e.g., TCO-based examples), the low-loss state was considered. Note that complete data was not always available and losses may be in some cases overestimated.^{172,179} Fig. 18(b) displays a large span for the $V_\pi L$ product, starting from ~ 100 Vmm in the case of LN-based phase shifters and reaching values as low as ~ 0.1 Vmm for TCO-based examples. The respective propagation loss spans many orders of magnitude as well. Introducing the loss-efficiency product $V_\pi L a_{\text{dB}}$ (units of VdB) as a figure of merit for phase-shifting efficiency [contour lines in Fig. 18(b)], LN,^{54,253} III-V,^{91,101,254} and EOP^{171,255} platforms demonstrate phase-shifting units of equivalent efficiency (~ 1 dB V), each striking a different balance between $V_\pi L$ product and propagation loss. Pure silicon-photonics demonstrations fall slightly behind and they can be roughly grouped in two main families depending on the employed control mechanism (depletion^{256–258} or carrier-injection^{110,259}). Depletion-based schemes provide higher $V_\pi L$ products, but also lower propagation losses. The lower right quadrant of Fig. 18(b) is mainly occupied by plasmonic EOP^{178,179} and TCO-based^{204,205,208} phase shifters, which achieve record-low $V_\pi L$ values, highly desirable for ultra-compact and/or low-voltage phase shifters. Their high propagation losses could be mitigated by restricting on-chip lengths to only a few microns, maintaining thus the total IL at acceptable levels.

Fig. 19 collectively compares experimentally demonstrated modulators at the component (modulator) level, reporting the ER/IL ratio (modulation efficiency) [Fig. 19(a)] and the EO bandwidth [Fig. 19(b)]. In-line (squares), MZI (triangles), and resonant (circles) modulator configurations are considered. One-to-one correspondence of data in the two panels of Fig. 19 is not always possible due to the lack of the corresponding data. We restrict the comparison to the case of amplitude modulators. For the ER/IL ratio [Fig. 19(a)], ER is the static ER and IL refers to the total on-chip loss, ex-

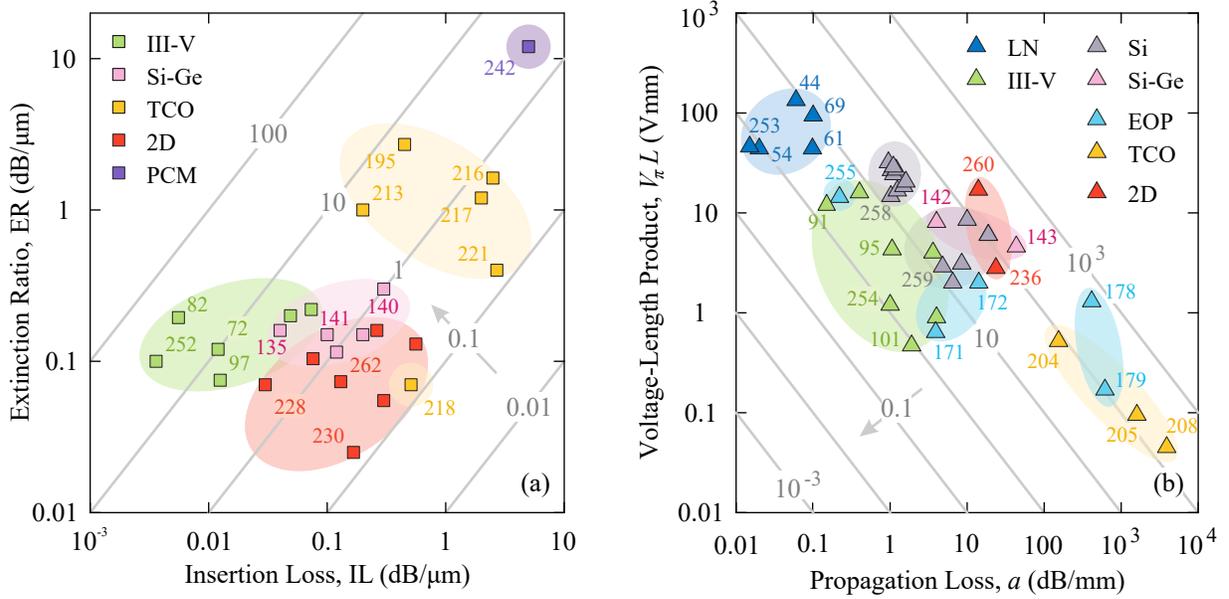


FIG. 18. Comparative assessment on the level of modulating unit. Efficiency of (a) loss- and (b) phase-modulating units, as extracted from experimentally demonstrated EO modulators in the literature. (a) Extinction ratio (dB/μm) as a function of introduced insertion loss (dB/μm) for loss-modulating units employed in waveguide amplitude modulators. Contour lines correspond to the modulation-efficiency figure of merit given by the ratio ER/IL. (b) Voltage-length product as a function of propagation loss for phase-shifting units, either standalone or integrated in MZI and resonant modulators. Contour lines correspond to the modulation-efficiency metric $V_{\pi}La_{dB}$ (units of V dB). Arrows in each panel indicate the direction of higher modulation efficiency. Characteristic references are included as well.

cluding coupling losses from input/output grating couplers. The basis of comparison (horizontal axis) is the *interaction length*, defined as the length of interaction between the guided wave and the configurable material (e.g., the length of the phase shifter in an MZI arm or the arc in the circumference of a ring resonator where the configurable material is integrated). It is used in place of the footprint value, which cannot be safely calculated across the different experimental setups, where additional components such as grating and directional couplers or metallic contacts and TWEs are included. This is particularly evident in the case of MZMs, where the employed Y-junctions or MMIs contribute significantly to the overall component footprint. Since these contributions are not related to the modulation effect itself, we consider the interaction length as a more appropriate basis of comparison.

Two regimes are identified in Fig. 19(a); the majority of MZMs populate the right side of the panel, leaving in-line and resonant modulators on the left side. This is due to the bulky size of MZMs as a result of the requirement for phase accumulation. On the other hand, their interferometric operation grants them with high ER values well-exceeding 20 dB for an IL penalty of only a few dB.^{54,61,253} Overall, their modulation efficiency does not drop below unity, with the exception of TCO-based MZMs,^{204,205} which are challenged by maintaining both acceptable ILs (short interaction length) and low driving voltages to avoid dielectric breakdown.

In-line and resonant modulators are considerably more compact, which is very attractive for on-chip applications. Their modulation efficiency can be comparable to that of MZMs (reaching ~ 10), even though challenges exist for the

less mature material platforms. Samples of higher quality will reduce IL and improve the modulation efficiency. While in-line modulators leverage changes in the mode loss, resonant configurations are more suitable for material systems with a predominately electro-refractive behavior. Such materials include the conventional Si¹²⁰ as well as novel systems as the WS₂ monolayer.²⁶⁰ Resonant modulators capitalize on the enhanced interaction between the guided mode and the configurable material in the cavity. Their performance, however, is sensitive to shifts in resonance wavelength due to fabrication imperfections or thermal effects (in contrast to the more robust behavior of in-line modulators). Such shifts can be usually externally corrected with heaters at the expense of increased complexity and energy consumption.

In terms of EO bandwidth, Fig. 19(b) compiles and presents EO modulators with an experimentally demonstrated bandwidth greater than 1 GHz as a function of interaction length. Values around 50 GHz or above have been measured by the majority of the considered material platforms, including in-line EAMs based on III-V,^{80,82} Ge¹³³ and SiGe¹⁴¹ semiconductors, as well as MZMs with phase shifters employing LN,^{44,54,61,253} III-V,^{89,91,261} Si,¹¹² SiGe,¹³³ and EOPs^{171,179,255} in addition to an Si-based ring modulator.¹²² Such values are suitable for achieving bit rates higher than 100 Gbps by simply employing a 4-PAM modulation scheme. Quite fewer modulators have demonstrated an EO bandwidth exceeding 100 GHz, including a III-V-based EAM⁸² and two LN-based MZMs,^{44,54} with the record-high 170-GHz EO-bandwidth of Fig. 19(b) belonging to the plasmonic EOP-based MZM reported in Ref. 179. Regarding the less mature

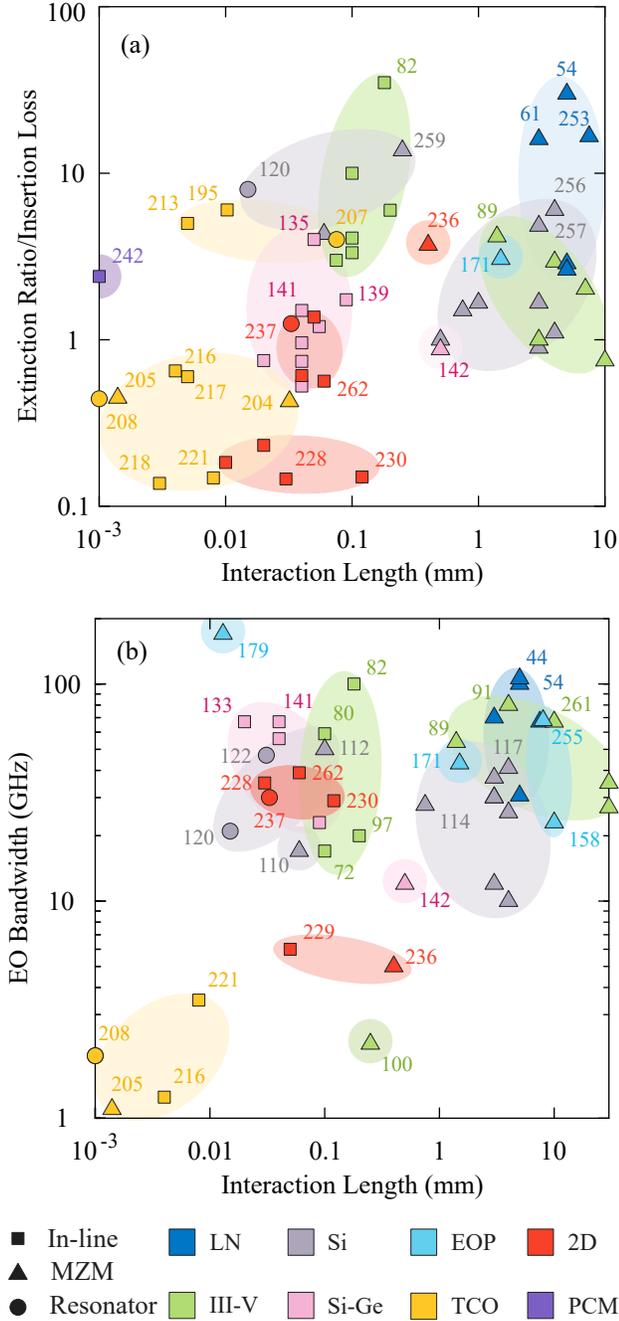


FIG. 19. Comparative assessment on the level of component. Performance metrics of demonstrated EO amplitude modulators employing in-line, MZI, or resonant configurations. (a) Ratio between (static) ER and IL and (b) EO bandwidth, both as a function of the interaction length between the guided wave and the configurable material. Characteristic references are included as well.

material platforms, an EO bandwidth as high as 39 GHz has recently been demonstrated for a double-graphene EAM in Ref. 262, while the experimentally measured EO bandwidth of TCO-based modulators has yet to exceed 3.5 GHz.²²¹ Similarly, a III-V/Si MOS capacitor MZM has demonstrated an EO bandwidth as high as 2.2 GHz,¹⁰⁰ rather limited by the

inefficient delivery of the RF signal.

Observing Figs. 18 and 19 from a materials perspective, conventional options such as LN, III-V, or Si are suitable for EO modulators of high modulation efficiency ($ER/IL > 10$) with bandwidth values exceeding 50 GHz, allowing thus for bit rates greater than 100 Gbps even with the aid of simple, low PAM modulation formats. However, they fall behind in terms of the $V_{\pi}L$ product and footprint when compared with contemporary options (TCOs, EOPs, 2D materials). The continuing research for novel materials is justified by the desire for reduced footprint, with the challenge being the experimental demonstration of low-loss and high-bandwidth modulators; both shortcomings related to the quality of the demonstrated samples, an issue that we believe will be resolved as the underlying physical effects become better understood and the fabrication techniques more mature. Of equal importance is the energy consumption of EO modulators, which, despite its significance, has not yet been discussed for reasons that are explained in detail in Section IV D.

B. Modeling Considerations

The accurate modeling of the underlying physical phenomena is fundamental for fully understanding and correctly quantifying the modulation effect. This is essential for efficiently designing the EO modulators and realistically evaluating their modulation performance before actually fabricating the components. It has been observed that even for the same material platform theoretical predictions can often vastly differ, occasionally reporting contradicting predictions; in addition, correlation of experimental to numerical data often appears very poor, with the experimental performance appreciably falling behind theory. This has been the case, for example, with graphene- and TCO-based modulators and strongly relates to the theoretical description of the underlying physical effects. In many instances, the origins of such discrepancies are traced to lack of rigor in adopted theoretical models.

Early works in graphene-comprising modulators treated graphene as a thin layer of bulk material, whose thickness was set from 0.33 nm up to a few nanometers, subject to the number of graphene layers assumed as well. Its optical properties were expressed in terms of an effective bulk permittivity. The ascribed value highly depended on the assumed thickness, as well as the adopted surface conductivity from which it originates, leading to ambiguous material properties. Initially graphene was considered as a thin *isotropic* bulk medium; this led to an artificial interaction with the electric-field component lying normal to its plane. As its chemical potential was varied and the effective permittivity shifted from dielectric to metallic values, a spurious ENZ effect was manifested (resembling the case of TCO materials), resulting in unnaturally impressive modulation metrics, especially for large values of its thickness.^{263–265} Clearly, the most natural representation of graphene that avoids such issues is as a 2D conductive sheet that can support a surface current and that is characterized by a complex surface conductivity (measured in Siemens). This approach is more straightforward, unambiguous, and compu-

tationally efficient.²⁶⁶ Nevertheless, it has been shown that the effective bulk medium description can also be accurate as long as the thickness is set to physically reasonable values (~ 1 nm), and that the physical anisotropy of graphene, i.e., the inability to support an out-of-plane current, is correctly considered.²⁶⁷

On another topic, most publications in TCO-based modulators adopt approximate techniques for the description of the developing carrier density and for mapping the carrier density to variations in the TCO optical permittivity. Fig. 20 outlines the three approaches that are followed in the literature. The simplest possible technique, Fig. 20(a), is that of appointing a step change in carrier density under the field effect; carriers form an accumulation layer of thickness t_{acc} , outside of which carrier density acquires the flat-band level. Selection of t_{acc} is rather arbitrary, with values ranging between 1 and 10 nm, strongly impacting the computed performance, while no relation between the externally applied voltage and the constant carrier density $n_{e,\text{acc}}$ can be established due to ignoring the underlying electrostatic problem. In the second alternative of Fig. 20(b), a relation between the carrier density $n_{e,\text{acc}}$ and the applied voltage V_a is introduced either by using an equivalent capacitor model^{198,201,209} or by averaging the spatial distribution of the carrier density across the TCO thickness $n_e(x, V_a)$, calculated with the aid of a semiconductor model (for instance the Thomas-Fermi). The carrier distribution $n_e(x, V_a)$ is then weighted over the layer thickness t_{acc} , resulting in an average carrier density $n_{e,\text{av}}(V_a)$ for the accumulation layer.^{192,194,195} The above two alternatives are extensively used in the TCO-modulator literature due to their computational ease. Only recently have rigorous methods for calculating the carrier density relying on semiconductor physics been introduced, including semi-classical^{187,203} or quantum descriptions.^{211,212} These studies introduce in the wave optics the exact spatial dependence of the carrier density $n_e(x, V_a)$, as obtained by the semiconductor model of choice [Fig. 20(c)]. The spatial dependence of the carrier density in Fig. 20(a)-(c) is translated in variations of both the real and imaginary part of the optical permittivity, as qualitatively illustrated for the real part in Fig. 20(d)-(f), employing a carrier-dependent dispersion model.

It is very important to stress that the approaches of Fig. 20(a)-(b) frequently lead to overly-optimistic or even unrealistic modulator performance, when compared against the approach of Fig. 20(c) that is based on more rigorous semiconductor models. In Fig. 21, the step-change approach is directly compared to the more rigorous results of the drift-diffusion model for an ITO-loaded silicon structure.¹⁸⁷ A pronounced deviation is evidenced between the two calculations in terms of not only the intensity of the modulation effect, but also the dependence on the applied bias. Additionally, the step-change method is proven highly sensitive to the thickness of the accumulation layer, resulting in a significant overestimation of the ENZ effect as illustrated in Fig. 22(a) for the case of a hybrid-plasmonic waveguide and thickness values up to 3 nm.²¹¹ A 10-nm accumulation layer in the TCO-loaded Si-slot modulator of Ref. 200 results in a $17.4 \text{ dB}/\mu\text{m}$ ER, further enhanced to $46 \text{ dB}/\mu\text{m}$ in the case of a plasmonic waveguide.¹⁹⁷ Simi-

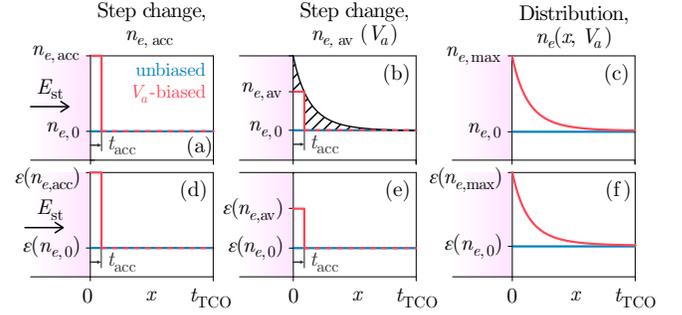


FIG. 20. Techniques for representing the TCO free-carrier concentration under the field effect. (a,b) Step index change over a thickness of t_{acc} from the flat-band value of $n_{e,0}$ to $n_{e,\text{acc}}$ and $n_{e,\text{av}}(V_a)$, respectively, under the effect of field E_{st} . (c) Continuous distribution $n_e(x, V_a)$ with exponential decay from the peak located at the TCO/insulator interface to the $n_{e,0}$ level. (d-f) Respective changes in the optical permittivity profile (real part only).

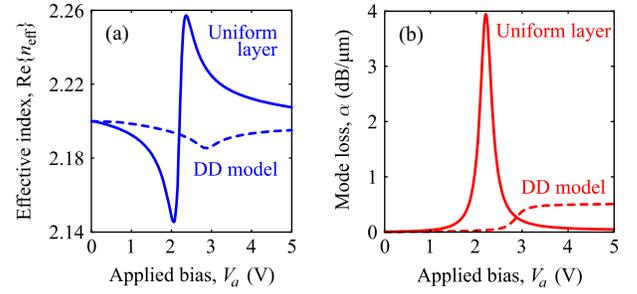


FIG. 21. Comparison between the step-change approach (uniform layer) and the drift-diffusion (DD) model for (a) the real part of the effective index and (b) the mode loss in an ITO-loaded silicon structure. Adapted, with permission, from Ref. 187 Copyright 2018 IEEE.

larly, a $V_{\pi}L$ product as low as 0.004 V mm has been reported for the hybrid-plasmonic waveguide in Ref. 209. Such performance metrics are at least overoptimistic and cannot be practically anticipated.

Fig. 22(b) collectively presents a comparison among the theoretical descriptions of the step-change approach, the drift-diffusion, and the quantum moment models in the case of a hybrid-plasmonic waveguide.²¹² All three approaches predict a different dependence for the mode loss of the guided mode as a function of the applied bias. The comparison to the included experimental results indicates that the quantum model seems closer to the measured data for this case, but a strong agreement is yet to be reported. Besides, nanometer or even subnanometer dimensions might require the consideration of quantum phenomena to accurately describe the effect. Non-local electron response²⁶⁸ and quantum phenomena such as electron spill-out and quantum tunneling²⁶⁹ can become important, as has been experimentally observed for example in plasmonic systems with subnanometer gaps.²⁷⁰ They can be taken into account through “quantum-informed” semiclassical calculations,²⁷¹ or quantum methods such as the density functional theory.

Moreover, fine structural features and thin material layers,

encountered in all contemporary EO modulators, have material properties that can deviate from the typical bulk permittivity description, which should be taken into account when performing calculations. Thin-film instead of bulk material properties need to be adopted where necessary. For example, in thin metallic films, material properties start deviating from the bulk values for thicknesses below 100 nm and at values of 20 nm or 10 nm they can differ significantly.²⁷² Similar deviations are evidenced for the optical and electrical properties of semiconductor materials, whose parameters can also depend on the deposition conditions as well as the underlying substrate. Specifically, in the case of the non-stoichiometric TCO semiconductors, controlling the oxygen concentration through the respective pressure can affect significantly their optical properties and shift the ENZ wavelength.²⁷³ Furthermore, high-quality nm-thick TCOs can be proven particularly challenging. In Ref. 274, ultra-thin ITO films deposited on a silicon substrate have formed discontinuous islands for thicknesses below 15 nm due to becoming thinner than the critical percolation threshold. Decreasing the film thickness from 100 nm to 10 nm results in decreasing mobility values, attributed to the formation of a non-electrical, ~ 14 -nm dead layer at the ITO/Si interface due to interfacial defects.²⁷⁴ Recalling the nm-thick ITO layers usually considered in theoretical works, these findings raise a significant uncertainty regarding the viability of the designed modulators. For this reason, high-quality material interfaces, with low trap and defect densities, are actively investigated, particularly for effectively integrating high- κ dielectrics into field-effect devices.^{275,276}

The theoretical description of newly reported materials can also be sometimes a challenge. This is the case for graphene, whose surface conductivity is frequently a controversial topic. Although it is almost universally accepted that it is described by a Kubo formula,^{277,278} introducing the physically correct parameters in the formula is not always straightforward. Amongst others, graphene conductivity depends on the Fermi level of the electrons and on phenomenological relaxation times of the various electron scattering mechanisms. The lat-

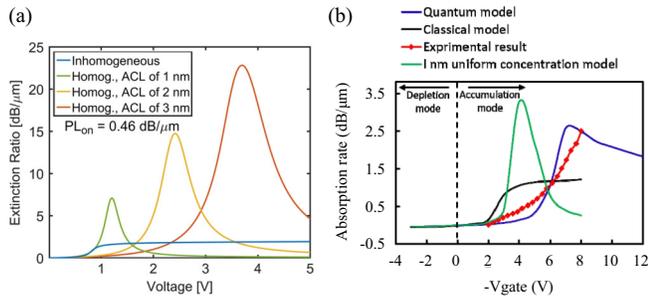


FIG. 22. (a) Comparison between the step-change approach and a more accurate inhomogeneous model (quantum hydrodynamic) for three values of the accumulation-layer thickness.²¹¹ Adapted, with permission, from Ref. 211. Copyright 2016 IEEE. (b) Collective illustration of the step-change approach, a classical (drift-diffusion), and a quantum model in comparison to experimental results.²¹² Both figures refer to a hybrid-plasmonic waveguide. Adapted with permission from Ref. 212. Copyright 2018 The Optical Society.

ter quantify graphene purity and is difficult to be *a priori* estimated, while the former strongly depends on the underlying structure upon which graphene is placed and should also be chosen carefully.²⁷⁹

In addition, the strong dispersion exhibited by some EO materials like TCOs and graphene near their ENZ and half-photon-energy wavelength, respectively, has questioned the applicability of frequently employed methods in photonics. One of them concerns the correct calculation of the quality factor in structures based on resonant elements. The Q -factor is an important parameter of a resonant system since it measures the resonance linewidth and, consequently, it reveals the capability of high- Q resonators to provide high ERs only with minor shifting of the resonance frequency under external stimuli. Furthermore, the correct calculation of the Q -factor in resonant devices is crucial since their design is based on that knowledge, ultimately determining the resonance shape and its peak/deep amplitude; a characteristic example is the operation of a ring resonator under critical coupling (equality of intrinsic and coupling losses, mathematically captured by the respective quality factors), resulting in a theoretically zero transmission. Q -factor can be assessed via time-domain, frequency-domain (time-harmonic), and eigenvalue simulations; correct calculations are well-established for traditional photonic platforms (LN, III-V, Si) and various computational techniques have been proposed and accurately applied.^{214,280,281} Nevertheless, only recently it has been pointed out that in contemporary systems where highly dispersive materials are involved, traditional approaches fail to give the correct Q -factor and other alternatives should be adopted, such as the careful choice of the calculation method/computational domain combination²⁸¹ or the expansion of the simulated system with additional auxiliary fields.²⁸² Thus, to accurately design a resonant modulator involving highly dispersive materials, an appropriate computational approach which respects their dispersive nature should be followed.

C. Electrodes and Contacts

Efficient application of the RF signal highly depends on the design of the electrodes delivering the modulation signal as well as the quality of the metallic contacts. As discussed in Section III, two types of electrodes can be identified: traveling-wave (TW) and lumped electrodes. Electrically long modulators (e.g., LN-based MZMs) employ in principal TW electrodes, while short modulating units (e.g., based on TCOs) usually opt for lumped ones. A rough length criterion could be the one tenth of the free-space RF wavelength divided by the RF effective index. Both schemes aim the efficient modulation of the guided wave at minimum cost and maximum speed, with each one exhibiting, though, unique design challenges.

Traveling-wave electrodes require the often challenging matching between the phase velocity of the RF signal and the group velocity of the optical wave, while ensuring low losses for the propagating RF signal. Lumped electrodes are in prin-

ciple much simpler, but they should maintain their lumped operation even at high frequencies. This could limit the modulation efficiency in the case of weaker modulation effects, which require in principle longer interaction lengths. For example, adequately long modulating elements are necessary in order to achieve high ER values in the case of EAMs or the required phase shift for full extinction in an MZM. Longer phase shifters also allow for maintaining low drive voltage requirements. On the other hand, increased interaction lengths raise insertion losses, reducing the overall efficiency of the modulator as well as its optical modulation amplitude. Thus, the optimal interaction length often results as a compromise among multiple performance criteria.¹⁵

In both TW and lumped electrodes, the use of standard 50- Ω systems can also be proven problematic. As described in Section III, impedance matching is generally challenging for InP-based MZMs due to the large capacitance per unit length of the p-i-n waveguide structure. Lumped-electrode modulators are also challenged from the in-series 50- Ω impedance. The latter adds to the total resistance of the modulator, increasing the intrinsic RC-constant, reducing, thus, the maximum achievable EO bandwidth.¹⁵ Moreover, the small impedance values of lumped-electrode modulators can also hinder the efficient delivery of the RF signal. Improved matching can be achieved through TWE schemes, which transform the impedance to a higher input value. Alternatively, the use of low output impedance lumped-element drivers is suggested.

Electrodes should be placed in close proximity to the modulating area in order to eliminate the series resistance from the contact to the core of the modulator as well as parasitic-capacitance effects, both of which hinder the efficient delivery of the modulation signal, raise the energy consumption, and limit the modulation speed. At the same time, however, metallic as well as highly doped semiconductor parts should lie sufficiently far from the guided optical wave to avoid excessive optical losses; this further extends to the contacts themselves. Techniques such as gradual doping¹¹⁷ are often employed as a compromise between reducing series resistance and maintaining low ILs.

Contacts themselves can also impose significant limitations to the overall modulation performance. Even though the ideal ohmic representation is commonly considered in energy-consumption and bandwidth calculations, this approach is elementary and can only provide an estimate for the upper performance limit. In practice, metal-semiconductor interfaces often form Schottky junctions, further raising the series resistance; thus, metals that exhibit a zero or even a negative Schottky barrier Φ_B with the underlying semiconductors are favorable. For example, aluminium or copper contacts are used with Si,^{114,117,126,168,170} gold with TCOs,^{204,221} and platinum or nickel with InGaAsP.^{283,284} Nevertheless, the series resistance reduction is not easily achievable and sometimes stacked metals are used (e.g., Ti/Pt/Au,²³⁶ Au/Cr,^{174,229} Ti/Au,^{49,219} Au/Ge/Ni,¹⁰⁰ etc.), complicating the fabrication process. On the contrary, metal-insulator interfaces are less challenging, with gold and aluminium contacts being popular among modulators based on the Pockels effect.^{44,54,157,158} The

complexity of electrical contacts usually discourages their rigorous consideration in theoretical calculations and is therefore ignored, resulting in more generous estimates for the energy consumption and bandwidth metrics.

D. Energy Consumption

One of the key performance metrics of a modulator is its energy efficiency, usually expressed through the energy required for the transmission of a single bit of information. Its rigorous theoretical calculation is of utmost importance since it provides an estimate for the lowest anticipated consumption in practice. Landmark works in the field of EO modulation have become points of reference for understanding the origins of energy dissipation.¹⁶ However, newly reported modulators often overlook such a precise (and sometimes cumbersome) calculation or perform rough estimates, which regularly result in record-low but unrealistic values. Here, we review key considerations for calculating the energy-per-bit performance for the indicative case of lumped-electrode, field-effect OOK modulators, which identify with some of the most promising designs reviewed in Section III. Erroneous practices encountered in the literature are also highlighted. In the examined cases, the electric field is introduced either through a reversely biased p-i-n junction or a MOS-like structure. Note that in the case of TWE designs, a transmission-line-theory description should be employed.^{67,120,285}

The calculation of the energy-per-bit value E_b is usually limited to the energy $E_{b,el}$ required for electrically charging/discharging the modulator. However, an equally important contribution originates from the actual optical energy required at the transmitter as well as the energy dissipated due to optical-loss mechanisms. Both contributions are collectively included in a term $E_{b,opt}$. The total energy-per-bit consumption is then expressed as $E_b = E_{b,el} + E_{b,opt}$.

Starting with the calculation of $E_{b,el}$, it originates from the dissipation in the resistive parts of the electrical junction due to the current flow during charging/discharging. Among the four possible state transitions, “0-0”, “0-1”, “1-0”, “1-1”, only one complete charge/discharge cycle (“0-1”, “1-0”) is formed. The average dissipation per bit, thus, equals, $E_{b,el} = 1/4 \times E_{el}$, where E_{el} is the total energy dissipated during a full charge/discharge cycle. The factor of one quarter is of statistical origin and equals the probability of a complete charge/discharge cycle in an equiprobable modulating signal.

Assuming the worst case scenario and the lack of an external recovery circuit, the total electrical dissipation E_{el} equals twice the stored energy in the structure, $E_{el} = 2 \times E_{stored}$.¹⁶ The stored energy is usually calculated using the simple, ideal parallel-plate capacitor model, $E_{stored} = C_g V_a^2 / 2$, with $C_g = Q/V_a$ denoting the *geometric* capacitance of the junction, V_a the applied bias, and Q the total charge. For simplicity, the junction is considered to be driven between the unbiased state and a maximum value V_a in the absence of any static bias.

However, revisiting the definition for the stored energy, it

holds that

$$E_{\text{stored}} = \int_Q V dq, \quad (1)$$

where $Q = \int_{\Omega_1} \rho d\Omega$ is the total charge in the structure, with ρ being the space charge density in one plate of the capacitor (volume Ω_1) as long as the difference in the electrostatic potential between the two capacitor contacts is correspondingly defined as $V = \phi_{t,1} - \phi_{t,2}$. From Eq. (1), it is concluded that the ideal parallel-plate capacitor model presumes a junction of constant capacity C_g so that $Q = C_g V$. This is the case, however, for a capacitor whose plates have a high density of states (DOS) and are, thus, considered as equipotential volumes (e.g., metals). Practical junctions in photonic components are most usually of semiconductor nature, with the electrostatic potential decreasing exponentially toward the interior of the semiconductor under the application of an external electric field until reaching its flat-band value (screening effect). The actual potential drop in the semiconductor, $V_{\text{semi}} = \phi_t - \phi_s$ (ϕ_t is the electrostatic potential at the contact and ϕ_s at the semiconductor–insulator interface), and the developed charged Q are interrelated quantities, allowing for defining a bias-dependent semiconductor capacitance $C_{\text{semi}}(V_{\text{semi}}) = \partial Q / \partial V_{\text{semi}}$, which contributes in series to the geometric capacitance. Expressing the total capacitance as $C_{\text{tot}}^{-1} = C_g^{-1} + \sum_i C_{\text{semi},i}^{-1}$, Eq. (1) is rewritten as

$$E_{\text{stored}} = \int_0^{V_a} C_{\text{tot}}(V) V dV. \quad (2)$$

For the calculation of the electrostatic potential and the space charge density, a rigorous solution of the underlying semiconductor problem is required. A fully equivalent, field-based expression is derived in Ref. 286,

$$E_{\text{stored}} = \int_{\Omega - \Omega_{\text{semi}}} \frac{1}{2} \mathbf{E} \cdot \mathbf{D} d\Omega - \oint_{S_{\text{semi}}} (\phi_s - \phi_t) \mathbf{D} \cdot d\mathbf{S}, \quad (3)$$

with \mathbf{E} (\mathbf{D}) denoting the electric field (electric displacement), ϕ_s (ϕ_t) the surface potential at the semiconductor–insulator (contact–semiconductor) interface, and Ω_{semi} (S_{semi}) the volume (external surface) of the semiconductor parts of the junction. Consequently, apart from the well-known and usually solely considered first term, there is an additional contribution to the total energy, which stems from the formation of non-neutral, space charge regions in the semiconductor materials. It results from their lower DOS, in contrast to high DOS and equipotential metals, where it vanishes ($\phi_s \equiv \phi_t$). Its effect becomes even more pronounced in junctions comprising low-dimensional materials such as graphene. There, the injected carriers occupy increasingly higher energy levels as a result of the Pauli blocking, leading to an increase in the internal chemical potential μ_c of graphene. The latter is expressed as the difference in the total electrostatic potential, $\mu_c = e(\phi_s - \phi_t)$.²¹⁴ The new term in Eq. (3) reduces to

$$- \oint_{S_{\text{semi}}} (\phi_s - \phi_t) \mathbf{D} \cdot d\mathbf{S} \rightarrow - \frac{\mu_c}{e} Q_s, \quad (4)$$

where $Q_s = -e \int n_s dS$ is the surface charge density on graphene and n_s the net carrier density between electrons and holes, related to μ_c through Fermi-Dirac statistics. The stored energy in graphene can be equivalently represented by a *quantum capacitance* $C_q = \partial Q_s / \partial V_{\text{ch}}$, in series to the geometric capacitance C_g of the structure, where $V_{\text{ch}} = \mu_c / e$ is defined as a local channel electrostatic potential.²⁸⁷ The effect of C_q is frequently overlooked in graphene-comprising junctions, underestimating their energy requirements as well.

After having calculated the stored energy at steady-state conditions through Eqs. (1)–(3), the total dissipated energy due to charging/discharging E_{el} equals twice this value. Opting for a time-dependent solution scheme, this energy could be alternatively calculated by integrating the absolute value of the power $W = VI$ provided by the source (or returned to it) during charging (discharging). No external recovery circuit is employed and, thus, the energy returned to the source is dissipated, generating heat.¹⁶ Letting T being the time period of a complete charging/discharging cycle, the total electrical dissipation can be expressed as

$$E_{\text{el}} = \int_0^T |V(t)I(t)| dt, \quad (5)$$

where $V(t) = \phi_{t,1}(t) - \phi_{t,2}(t)$ is the time-dependent electrostatic potential difference between the contacts of the junction and $I(t) = \partial Q / \partial t$ is the total current flow. Eq. (5) should result in twice the stored energy at steady-state conditions [Eqs. (1)–(3)], provided that the bit period exceeds the minimum required time T for a complete charging/discharging cycle of the structure. Equivalently, the power W provided by the source (or returned to it) can be calculated as $W = dE_{\text{stored}}(t)/dt$, with the stored energy E_{stored} being now expressed as a function of time using any of Eqs. (1)–(3). Integrating the absolute value of W over the bit period T , one should arrive again at E_{el} . A point of attention when switching from steady-state to temporal calculations is the generic dependency of capacitance on the frequency of the applied signal, with its high-frequency value being reduced compared to its low-frequency one. This effect manifests predominately under inversion conditions and originates from the inability of minority carriers to respond to increasingly alternating fields.

For reasons of comprehensiveness, we take a step further towards a brief discussion of the key points for calculating the optical energy per bit $E_{b,\text{opt}}$ required at the transmitter, with a detailed analysis presented in Ref. 16. Employing an EAM as a case study, let $E_{\text{in},o}$ be the total optical energy introduced to the modulator during the period of a symbol. The dissipated energy can be distinguished in the energy absorbed E_{abs} , but also in the energy E_{pc} associated with the photocurrent flow as a result of the generation of free carriers and their sweeping in the reverse-biased diode structures. Accepting the generation of one electron-hole pair for each photon absorbed (perfect quantum efficiency), the total generated charge equals $Q_{\text{pc}} = eE_{\text{abs}}/\hbar\omega$, collected under a reverse (or small forward) bias V . For each modulation state, the dissipated power can be expressed as

$$E_{\text{diss}} = E_{\text{abs}} + E_{\text{pc}} = E_{\text{abs}} + \frac{eE_{\text{abs}}}{\hbar\omega} |V|, \quad (6)$$

where V is the reverse bias applied to the diode. The transmitted optical energy equals

$$E_{\text{trans}} = E_{\text{in},o} - E_{\text{abs}}. \quad (7)$$

Considering the case of an OOK modulator, the energy $E_{b,\text{opt}}$ can now be expressed as the average energy dissipated and transmitted between the low and high energy states,

$$E_{b,\text{opt}} = \frac{1}{2} [(E_{\text{diss}} + E_{\text{trans}})_L + (E_{\text{diss}} + E_{\text{trans}})_H]. \quad (8)$$

Substituting Eqs. (6) and (7) in Eq. (8),

$$E_{b,\text{opt}} = \frac{1}{2} \left(2E_{\text{in},o} + \frac{eE_{\text{abs},L}}{\hbar\omega} |V_L| + \frac{eE_{\text{abs},H}}{\hbar\omega} |V_H| \right). \quad (9)$$

Introducing the absorption coefficient η so that $E_{\text{abs}} = \eta E_{\text{in},o}$ and the photocurrent dissipation multiplier $\mu = e|V|/\hbar\omega$, Eq. (9) can be expressed as

$$E_{b,\text{opt}} = \frac{1}{2} (2 + \eta_L \mu_L + \eta_H \mu_H) E_{\text{in},o}. \quad (10)$$

The final step consists in determining the required input optical energy $E_{\text{in},o}$ at the transmitter. Its value should ensure that the minimum required optical energy per bit $E_{b,\text{rec}}$ arrives at the receiver. Since the detection performance depends on the energy difference between the two logic states, $E_{b,\text{rec}}$ should be expressed as a function of the useful energy launched at the transmitter. In the case of an ideal modulator ($\eta_H = 0$, $\eta_L = 1$), the average useful launched energy per bit equals $E_{b,\text{trans}} = E_{\text{in},o}/2$. However, in practical modulators, the energy difference is shrunk and $E_{b,\text{trans}}$ reduces to

$$E_{b,\text{trans}} = \frac{1}{2} (\eta_L - \eta_H) E_{\text{in},o}. \quad (11)$$

Using Eqs. (10) and (11), the average optical energy per bit is expressed as

$$E_{b,\text{opt}} = \frac{2 + \eta_L \mu_L + \eta_H \mu_H}{\eta_L - \eta_H} E_{b,\text{trans}}. \quad (12)$$

Considering the best case scenario of a zero-loss transmission, $E_{b,\text{trans}}$ will equal the required energy $E_{b,\text{rec}}$ at the receiver, which is determined by the specifications of the photodetector. The total energy-per-bit value is finally calculated as

$$E_b = E_{b,\text{el}} + E_{b,\text{opt}} = \frac{1}{4} E_{\text{el}} + E_{b,\text{opt}} = \frac{1}{2} E_{\text{stored}} + E_{b,\text{opt}}. \quad (13)$$

Quoting the calculations for the InGaAsP-embedded photonic-crystal EAM in Ref. 77 for a scale estimate, a total of $E_b = 4.3$ fJ/bit is calculated for the 105- μm design, from which $E_{b,\text{el}} = 1.6$ fJ/bit and $E_{b,\text{opt}} = 2.7$ fJ/bit (0.2 fJ/bit attributed to photocurrent dissipation), when a $E_{b,\text{rec}} = 0.5$ fJ/bit specification is defined at the receiver. It is evidenced, thus, that the required optical energy at the modulator can have a considerable contribution to its total energy consumption despite being frequently disregarded. Given also the low wall-plug efficiency of NIR semiconductor lasers

(the power ratio of the optical output to the required electrical input), currently around 15%,²⁸⁸ it can be argued that the optical term $E_{\text{in},o}$ in Eq. (9) has to be suitably weighted to reflect the increased cost of losing optical energy compared to a direct loss in electrical one as quantified by $E_{b,\text{el}}$ and the last two photocurrent terms in Eq. (9).

In practice, the share of the required optical energy at the modulator in its total energy consumption is further increased, when additional sources of loss are considered such as coupling losses between the laser and/or the input/output waveguides and the modulator, free-carrier absorption losses due to the overlap of the guided mode with doped semiconductor regions, etc. These additional optical losses result in a raise of $E_{\text{in},o}$ for maintaining the same $E_{b,\text{rec}}$ specification at the receiver. For the previous example of PhC-EAM,⁷⁷ considering an additional on-chip IL of 3 dB doubles the required $E_{\text{in},o}$, resulting in a twofold increase in $E_{b,\text{opt}}$ based on Eqs. (11) and (12), raising the total E_b to 7 fJ/bit. However, given the low efficiency of the respective laser source, this value could be in reality quite higher.¹⁵ It is important, therefore, to account for all major sources of dissipation in the calculation of the energy consumption so as to perform a fair comparison among markedly different modulation technologies (e.g., Pockels effect and QCSE) and physical systems (photonic, plasmonic), which are characterized by quite different levels of optical loss. Additional contributions to the total energy consumption, and specifically to the $E_{b,\text{el}}$ term, could include the energy dissipated for temperature control either for tuning or cooling operations. The latter can be avoided in the case of robust and athermal modulators, which therefore appear promising for future ultra-low energy applications.

The very ambitious goal of 10 fJ/bit or even lower has been set for the total energy consumption of future optical interconnects with lengths from ~ 1 cm to ~ 10 cm.¹³ Therefore, the consumption of the respective modulators should be limited to 1 fJ/bit or below. Strong indications for such a low consumption has been evidenced in the case of, e.g., a modulator based on a silicon-photonic resonator,¹²⁰ an Si-slot SOH MZM,¹⁶⁹ a plasmonic slot waveguide filled with an organic EO material,²⁸⁵ a double-layer graphene silicon waveguide,²²⁸ and an InGaAsP-embedded photonic-crystal waveguide.²⁸⁹ In the latter case, a record-low consumption of 42 aJ/bit is estimated. All these modulators demonstrate bandwidth values at least in the order of tens of GHz. However, such an assortment of literature works is not always directly comparable since, in some cases, the energy-per-bit value is theoretically calculated rather than actually measured, based on simple models that neglect parasitic effects and can significantly underestimate the energy consumption. In addition, the majority of works considers almost exclusively the dissipation due to the charging/discharging operation, disregarding the occasionally high ILs. Thus, a rough comparison can often be deceptive. A representative example are plasmonic modulators, which report much lower energy-per-bit values compared to purely photonic platforms thanks to their considerably smaller footprint, which reduces the charging/discharging dissipation, failing, however, to account for their considerably higher optical losses. In con-

clusion, promising future directions toward energy efficient modulators should include the use of a strong EO effect, such as the Pockels effect in polymeric materials of extraordinary EO coefficient or the QCSE in MQW structures, as well as an efficient compromise between strong optical confinement and low optical losses, using for example the silicon-slot or photonic-crystal platforms.

E. Reliability

The practical success of an electro-optic modulator platform also depends on reliability issues, given that devices in lightwave communication systems are required to operate reliably for at least 20 years in field conditions. Very different issues of reliability apply to different types of modulators. For instance, InP modulators may suffer from leakage current problems or phase changes, quantified in hard-aging conditions.⁹¹ Lithium niobate modulators may suffer from mechanical strain attributed to the electrodes (which is thermally dependent), pyroelectric charges that influence stability by shifting the operation point, and DC bias drifts related to dielectric relaxation processes.²⁹⁰ Electro-optic polymer modulators may suffer from stability issues, especially when operation at elevated temperatures is required; significant improvement is attained by employing EO polymers with high glass-transition temperatures.¹⁵⁸ Accelerated-aging tests, including high-temperature storage or temperature-cycling, are mainly conducted for assessing the reliability levels of modulators made of III-V semiconductors, silicon, lithium niobate or electro-optic polymers. Far less details on long-term stability or reliability are currently available for modulators based on 2D materials or transparent conducting oxides.

V. DIRECTLY MODULATED LASERS

Apart from external modulation schemes, directly modulated lasers (DMLs) are also commercially employed, especially for short-distance optical fiber communications. Recent developments in DMLs have highlighted their suitability for low-consumption and high-capacity optical interconnects as well.²⁹¹ Next, key elements of direct modulation are discussed together with indicative metrics of their state-of-the-art modulation performance.

First demonstrated in the 1970s,²⁹² DMLs use variations of the driving electrical current to modulate the intensity of the emitted light. Changes in the carrier and photon density inside the optical cavity result in wavelength and power oscillation effects, respectively, which degrade the transmission characteristics (adiabatic chirp). In contrast, external electro-absorption modulators exhibit only transient chirping, manifesting during the “0-1” bit transitions, which results in phase changes for the transmitted wave.²⁹³ As a result, external modulators are preferable for high-speed, long-haul transmission distances at the low-loss wavelength of 1.55 μm . DMLs have been rather limited to 10- and 100-Gbps transmissions up to 100 km at the zero-dispersion wavelength of 1.3 μm .

A direct comparison with external modulators would only be possible in the context of optical transceivers, which means that in the case of external modulators one should take into account the performance metrics of the required laser source as well. In general, DMLs demonstrate higher output power, proportional to their bias current, and they are considered more cost effective. Their small footprint renders them also attractive for optical interconnects. However, decreasing the transmission distance also requires a reduction in their operating energy (equivalently their bias current) to values that could compete with those of their electrical counterparts. The bias current can be reduced by decreasing the active laser volume, maintaining this way the current density above the lasing threshold. The active volume can be decreased using structures of higher reflectivity that increase the optical confinement, ensuring additionally an adequately high quality factor despite the reduction in the cavity volume. Moreover, a larger differential gain (the rate of the increase in gain with respect to the injected carriers) is also beneficial for the modulation efficiency and reduces the bias current. The differential gain depends on the underlying material platform, usually comprised of quantum-confined III-V-semiconductor structures, and it can be increased by enhancing the quantum confinement of carriers, the optical confinement, and the optical feedback from the reflector, as well as adjusting the detuning between the lasing wavelength and the gain peak wavelength.

The modulation bandwidth of DMLs is intrinsically limited by the relaxation oscillation frequency f_r , the damping effect, and the RC-constant. The parameter f_r refers to the frequency of the output-power oscillations until the laser stabilizes and it should be as high as possible. The damping effect suppresses the relaxation oscillation, mainly due to nonlinear gain effect, and it should be weak enough to avoid over-damping and a subsequent decrease in the achievable bandwidth. Frequency f_r increases with decreasing the active volume as well as increasing the differential gain and injection current. The latter implies a trade-off relation between energy consumption and bandwidth for the case of DMLs. Major factors that determine the damping effect are the nonlinear properties of the gain material, the differential gain, and the photon lifetime.²⁹⁴ Consequently, despite their simple modulation concept, low-energy and high-bandwidth DMLs constitute a challenging engineering problem.

DMLs can be in-plane structures, such as distributed-feedback (DFB) and distributed reflector (DR) lasers, or vertical-cavity surface-emitting lasers (VCSELs). The former use either a ridge waveguide or a buried mesa heterostructure and exhibit active lengths from 100 to 200 μm and modulation rates up to 56 Gbps (NRZ-OOK).^{295,296} InGaAlAs-based MQWs are usually employed for the active region due to their large conduction band offset, which allows for stronger quantum confinement and semi-cooled or even uncooled operation, which reduces the energy cost. A consumption in the order of a few pJ/bit is typically reported for DMLs. However, this value cannot be safely compared with the frequently reported fJ/bit values of external modulators since the latter also require a laser source, which is usually very inefficient.

Short-wavelength (850-1060 nm) multi-mode VCSELs are

based on the GaAs material system, which provides the opportunity for highly reflective GaAs/AlAs distributed Bragg reflectors (DBRs). GaAs-based VCSELs are characterized by low fabrication cost, a bandwidth exceeding 30 GHz, and an energy consumption of less than 100 fJ/bit due to a ten times smaller cavity volume compared to in-plane lasers.^{297–299} They are commercially used for short-reach (< 300 m) intra-datacenter optical links and supercomputers, limited by the modal dispersion. Transmission distances above 500 m can be achieved by few- or single-mode VCSELs, which, however, have limited output power and require more elaborate optical alignment.³⁰⁰ Long-wavelength VCSELs (1.3 or 1.55 μm) are based on the InP material system and target longer transmission applications. However, high-reflectivity InP-based DBRs are challenging and hybrid dielectric-semiconductor mirrors have been proposed instead. The reduced conduction band offset in InP-based MQWs negatively affects operation at high temperatures, while the precise control of the lasing wavelength is challenging due to fabrication imperfections of the wavelength-scale cavity length, hindering their use in wavelength-division-multiplexing (WDM) networks. The use of high-contrast gratings (HCGs) has been proposed as a possible solution.³⁰¹ InP-based VCSELs have demonstrated a bandwidth of 22 GHz, allowing for rates up to 50 Gbps (NRZ-OOK) for an energy consumption of 130 fJ/bit.³⁰² Current research on InP-based VCSELs targets the reduction of their active volume and the continuous tuning of their lasing wavelength.

Toward reducing the active laser volume, the photonic-crystal (PhC) platform provides significantly higher reflectivity values compared to the semiconductor DBRs used in VCSELs. PhC lasers demonstrate high- Q , ultra-small cavities together with the opportunity for a lateral current injection scheme.³⁰³ Heating issues are addressed by embedding the active region in an InP-based PhC line-defect waveguide. This kind of laser is referred to as a λ -scale embedded active-region PhC (LEAP) laser. A 10-Gbps rate with a consumption as low as 4.4 fJ/bit has been demonstrated for a LEAP laser with a 2.5- μm -long and 0.3- μm -wide active region.³⁰⁴ Despite the decrease in their operating energy, lasers with reduced active volumes usually provide low output optical powers that are often insufficient for typical detection schemes. Reducing optical losses through the monolithic integration of lasers with photodetectors as well as the minimum receivable optical power at the photodetector could ease the requirement for higher optical power. For this reason, photodetectors with capacitances in the range of tens of attofarads are targeted through their close integration with electronics.^{13,305}

In future, the integration of DMLs with silicon wafers can reduce significantly the fabrication costs. This can be achieved by methods similar to those described for the externally modulated lasers in Section III B. Differences in lattice constants and thermal expansion coefficients as well as the current injection scheme (vertical or lateral) should be considered for selecting the optimal integration method. Membrane DMLs with a rate of 25 Gbps (NRZ-OOK) and an ~ 100 fJ/bit operation have already been demonstrated.³⁰⁶

Overall, DMLs exhibit bandwidth values that typically do

not exceed 35 GHz,³⁰⁷ with commonly reported rates up to ~ 56 Gbps (NRZ-OOK).²⁹⁶ The use of advanced modulation formats (PAM, QAM, DMT), digital-signal-processing techniques as well as optical domain effects (e.g., optical feedback) can increase their modulation rate beyond 200 Gbps per lane.²⁴ In Ref. 308, a membrane DR laser on a silicon carbide substrate with a 50- μm -long active region exhibits a 60-GHz intrinsic bandwidth, enhanced to 108-GHz using the effect of optical feedback. The DML demonstrated a rate of 256 Gbps (4-PAM) with an energy consumption of 475 fJ/bit.

Spin-lasers have also attracted attention as an alternative means of overcoming the bandwidth bottleneck. By injecting spin-polarized carriers into conventional VCSELs, spin-lasers modulate the polarization of the output light instead of its intensity, exploiting the coupling between carrier spin and light polarization. The dynamics of polarization modulation has proven much faster compared to intensity modulation,³⁰⁹ allowing for a bandwidth exceeding 240 GHz at room temperature, which is nearly an order of magnitude greater than the respective of conventional DMLs.³¹⁰ In addition, the bandwidth is decoupled from the requirement for strong pumping, which dictates the performance of conventional VCSELs and results in increased heating and low efficiency. Spin-lasers can demonstrate their highest bit rates just above their lasing threshold, promising ultra-low power optical communication.

VI. FUTURE OUTLOOK

In this Section, the future prospects of the different EO materials employed in external modulators are discussed.

A. Lithium Niobate

The recent rapid developments in thin film lithium-niobate technology has created new prospects and revolutionized the LN modulator landscape. Despite the progress, $V_{\pi}L$ values below 1 V cm are still very challenging (excluding plasmonic devices). In 2020, the highest reported value for a uniform LN thin film loaded with a silicon nitride core was 2.1 V cm ($V_{\pi} = 0.875$ V, length 2.4 cm).³¹¹ Given the fixed r_{33} value, it is hard to anticipate spectacular improvements in this performance metric. However, further improvement is anticipated in integration techniques of LN thin films in silicon platforms, with high foundry compatibility achieved when the lithium niobate film is bonded on top of the silicon chip in an additional back-end step at room temperature.³¹² Demonstrations of IQ modulators operating at higher modulation formats in LN thin film technology are very limited and only very recently emerged. In 2020, Ref. 253 reported 110 GBd QPSK (220 Gbps) and 80 GBd 16-QAM (320 Gbps) in a monolithic device based on etched LN thin-film. In terms of other metrics such as the bandwidth-voltage ratio (BW/V_{π}), contemporary LN modulators can deliver values up to 25 GHz/V, which, however, fall behind those achievable with modulators based on electro-optic polymers.

With further reasonable improvements in various aspects, including the RF electrodes, bit-rates above 1.2 Tbps are anticipated, for instance raising the symbol rates above 200 GBd and using the 64-QAM format. Monolithic lithium niobate modulators (i.e., those based on etched LN thin films) will probably substitute in the near future the widespread conventional LN modulators. They outperform their conventional counterparts in every metric while being aligned with current requirements of reduced footprint, and are able to meet the demands of emerging applications. Hybrid LN modulators capitalizing on the silicon platform is a technology of medium term that is expected to further mature in the coming years, being a strong competitor to the purely silicon-based modulators that exploit carrier effects. Compatibility to the CMOS process is a prerequisite and related issues have to be resolved before extensive deployment can take place.

B. III-V Semiconductors

Since the first demonstrations of GaAs- and InP-based EAMs in the 1960s and 1980s, respectively, III-V-based modulators have become the conventional choice for long-haul optical communications at rates over 2.5 Gbps (OOK) due to the low or even negative chirp values. Today, state-of-the-art designs have experimentally demonstrated 100-GHz bandwidth values⁸² as well as bit rates up to 200 Gbps and beyond.²⁴ The reported 400-Gbps single-lane rate⁹² is very encouraging for next-generation 1.6-Tbps transmissions. Such rates will offer a significant reduction in the complexity of transmitters, which could be simply configured in a 4×400 -Gbps architecture, as well as simpler detection configurations. Current challenges for III-V-based modulators include their uncooled operation for reducing their energy consumption as well as a decrease in their driving voltages to CMOS-compatible levels (< 1 V) to avoid the need for amplification.⁷⁹

The intrinsic capability of III-V photonics to support the full spectrum of functionalities (lasers, amplifiers, modulators, detectors, routers, etc.) on-chip, with the aid also of high-speed III-V electronics, constitutes a competitive technological advantage. Intense research is carried out towards combining the benefits of III-V photonics with the low-cost and large-scale CMOS technology of silicon electronics and photonics. InP membranes bonded on SOI wafers appears as a promising means for adding photonic functions to silicon electronics.³¹³ The interconnection between the III-V photonic layer and the underlying CMOS-chip is achieved using vias through the BCB bonding layer, which provides reduced series resistances and thermal isolation. The InP-Membrane-on-Silicon (IMOS) technique does not interfere with the fabrication processes of electronic circuits and allows for devices of reduced footprint and energy consumption. Hybrid III-V/Si photonics consisting of a highly functional III-V layer bonded on top of a passive silicon-photonic circuitry have also attracted attention.⁹⁶ The outlook of III-V-based modulators appears also promising for future integrated quantum-photonic circuits, where direct-bandgap semiconductors, and particularly GaAs, are employed.³¹⁴

C. Silicon

Pure silicon modulators have undergone an immense evolution in the past 15 years, since the first GHz realization.¹⁰⁴ Transmission rates as high as 50 or 60 Gbps with a few dB dynamic ER are typically reported experimentally. Having reached the aforementioned speed limit, which is practically the upper achievable with the plasma dispersion effect due to the limited carrier mobility in Si,³¹⁵ interest has shifted toward more advanced modulation formats to further extend the data transmission rates; data rates as high as 400 Gbps have been demonstrated, despite the physically achievable upper limit of ~ 50 GBd. Transmission rates up to 1 Tbps are set as the next milestone of the platform. The maturity of the silicon platform is reflected in the current research topics, which mostly rotate around more commercial issues such as external bias minimization, power consumption limitation, footprint reduction, efficient monolithic integration with other passive or active components, packaging issues, etc. More fundamental concepts (e.g., interior modulator designs, modulation speed limits, etc.) are widely accepted as being well understood and conceptually resolved.

Interest is also shifting towards hybrid platforms which can bridge the advantages of silicon with other materials. This topic is extensively covered in Section III and is expected to gather the scientific and engineering effort in the foreseeable future. Compared with the pure silicon-on-insulator, hybrid platforms constitute a relatively new alternative and need significant improvement to reach maturity. However, given the promising prospects of hybrid integration, as already highlighted for the Si-LN, III-V-on-Si, and Si-Ge platforms, it constitutes one of the important scientific challenges for the next years.

D. Electro-Optic Polymers

EO polymer modulators evolved during a 25-year period and gradually reached impressive performance levels. Sub-1-mm length, sub-1-Volt half-wave voltage (V_π), bandwidth exceeding 100 GHz and loss in the range of 1 dB per phase-shifter can all be met in silicon-based implementations. Such modulators can be directly driven by a CMOS output, avoiding a driver amplifier that heavily contributes to the energy consumption, and making it possible to access the fJ/bit level. The possibilities for radically improved waveguide designs are limited and the slot waveguide appears as the optimal choice, featuring excellent optical confinement, almost perfect overlap between the optical and RF modes, convenient EO material poling by the RF lines, easy infilling, small footprint, and all advantages associated to the silicon platform. Plasmonic slot waveguides provide a successful route to device miniaturization and ultra-high BW; they are accompanied, however, by the well-anticipated increase in losses and the loss-efficiency product ($V_\pi L a_{dB}$) deteriorates by a factor of at least 10 with respect to photonic counterparts. It is hard to envisage any other waveguide that can encompass in the same degree the above set of features. Thus, future advance-

ments are mainly anticipated from improvements in EO materials, with progress in device engineering contributing to a lesser degree.

Advances in EO materials include self-assembled multi-chromophore dendrimers with maximized acentric order and minimized dipole–dipole interaction of the chromophores at high loading density, with the n^3r_{33} figure-of-merit outperforming traditional guest-host systems and reaching values as high as 2400 pm/V.³¹⁶ Although electro-optic activities in the range of 500–600 pm/V are possible in thick films,³¹⁷ in restricted geometries (such as in slot waveguides) the EO activity drops to values below 400 pm/V; this suggests that specific surface modification techniques are welcomed for shrinking dimensions. Further improvement in EO polymers will lead to devices that can efficiently and stably operate at elevated temperatures (as high as 100 °C), which is beneficial for installation in harsh environments or translates into reduced energy consumption for the cooling systems and lowers running costs in datacenters. Such devices were very recently demonstrated in Ref. 255, where a high glass-transition temperature is essential for the thermal reliability and long-term operation; T_g values exceeding 200 °C have already been reported.³¹⁸ Polling in sub-100 nm slots is a challenging process and improvements are also expected, as they will allow for higher acentric order in high number density materials. In view of all of the above, values of $V_\pi L \sim 0.1$ V mm are envisaged in the silicon platform with the corresponding metric dropping to ~ 0.01 V mm for EO polymer plasmonic modulators.

E. Transparent Conducting Oxides

During the last decade, the renewed interest in TCOs for NIR modulation applications has demonstrated modulators of record-low interaction lengths. In-line modulators with ER ~ 1.63 dB/ μm ²¹⁶ and MZMs with $V_\pi L$ products in the order of 0.095 V mm are among the most impressive experimentally verified performances. However, TCO-based modulators have exhibited high ILs, driving voltages from a few to tens of volts, and have yet to demonstrate the theoretically expected bandwidth values (> 100 GHz), being limited to a few GHz.

A bandwidth increase would require a reduction in the total RC constant by improving the quality of electrical contacts, optimizing the semiconductor doping profiles, as well as employing TCO films with high electron-mobility values. The latter has stimulated research toward nm-thick TCO films with improved electrical and optical properties, which usually differ from the respective bulk ones.^{274,319} Alternative compounds such as dysprosium-doped CdO (cadmium oxide) have also attracted attention for the same reason.^{221,320,321} High-quality TCO films could also reduce optical losses due to weaker damping of the oscillating free carriers. The use of silicon-photonics physical systems, instead of plasmonic or even hybrid plasmonic ones,²⁰³ could also mitigate ILs in TCO-based components, especially in phase-shifting units, since the lack of a predominately electro-refractive regime for TCOs inevitably results in high losses.¹⁸⁷ Currently, loss-

balancing mechanisms are employed to avoid ER-degradation in MZMs²⁰⁵ and ensure balanced constellations, free from any residual intensity-modulation effect.¹⁸⁷ Furthermore, the required driving voltages could reach CMOS-compatible levels by employing nm-thick dielectrics with high dielectric constant as well as breakdown voltage.³²² Such dielectrics would also be beneficial for a reduction of the energy consumption, which is currently estimated in the order of pJ/bit or below. In the years to follow, a convergence between theoretical calculations and experimental measurements will provide insight into the underlying physical effects and it will allow for alleviating the weaknesses of the demonstrated TCO-based modulators.

F. Two-Dimensional Materials

Modulators based on 2D materials are expected to remain in the research spotlight due to their high potential (large upper bandwidth limit, low footprint). Apart from the well-studied graphene, new 2D materials are gradually emerging, constantly renewing the interest in 2D-material-based modulators. High-ER and high-speed graphene modulators have already been experimentally reported, maintaining the interest for even greater metrics due to the inherently high carrier mobility in graphene. Theoretical predictions highlight potential EO bandwidths nominally exceeding 100 GHz,²⁶⁴ limited mostly by the RC-constant; at the same time, energy consumption from a few up to a few tens of fJ/bit is expected²⁶⁴ and has been experimentally reported.²³⁷ Generally, the optimum configuration of graphene upon a dielectric waveguide/resonator and the estimation of the upper performance metrics of amplitude and phase modulators is an open topic in the literature.^{214,323–326} All publications converge that graphene has the potential for ultra-high-speed modulation with high ER and acceptable ILs.

On the other hand, TDMs, MXenes, and BP, although exhibiting promising properties in principle, have more fundamental barriers to overcome, such as the achievement of efficient integration compatibility, accompanied with an overall low-cost and large-scale fabrication process. Besides, only recently BP- and WS₂-based modulators were realized for the first time, showing promising performance metrics.^{260,327} Low-cost and large-scale integration is an essential topic for graphene as well, since a wafer-scale transfer method for pure graphene sheets has yet to be achieved; the usage of reduced graphene oxide instead can be considered as an alternative approach.³²⁸ Finally, the most scientifically challenging but highly promising prospect is the design of Van der Waals heterostructures. Considering the numerous stacking variations, Van der Waals heterostructures are considered the future of 2D materials and can possibly enhance the performance of the respective modulators.

G. Phase-Change Materials

PCM-based modulators feature competitive performance in terms of footprint, extinction ratio, and insertion loss. How-

ever, they fall behind in switching speed, especially when the thermally-induced transition cannot be suppressed. Relatively high switching speed and low power consumption can be achieved by selectively exploiting the electronically-induced transition (nanosecond time scale) and successfully separating it from the much slower thermal one (microsecond time scale at best).^{243,329} This can be achieved by designing the electrical addressing circuit to limit the current flow through the material,²⁴³ minimizing Joule heating. This has a positive impact on energy consumption as well, but reduces the achievable extinction ratio. In addition, the experimentally observed hysteresis behavior seen in Fig. 17(b) is a limiting factor for modulator operation. As a result, there are still several open research questions to be solved in order for phase-change materials to compete with other alternatives and experience wider applicability.

VII. CONCLUSION

The state of the art in integrated electro-optic modulators has been reviewed, covering the different EO materials, integrated platforms, and physical phenomena that are involved in contemporary implementations. The current performance trends and the anticipated prospect of different material and modulator types has been highlighted. In addition, important physical aspects and frequently overlooked intricacies in the analysis and design of EO modulators have been identified and discussed.

AUTHORS' CONTRIBUTION

G. S. and T. C. contributed equally to this work.

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DATA AVAILABILITY

Most of the data included in this work is presented after consensual agreement of the corresponding author and/or the copyright owner, thus data sharing is not applicable. The data that exclusively created for this work is available from the corresponding author upon reasonable request.

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